

Figure 1

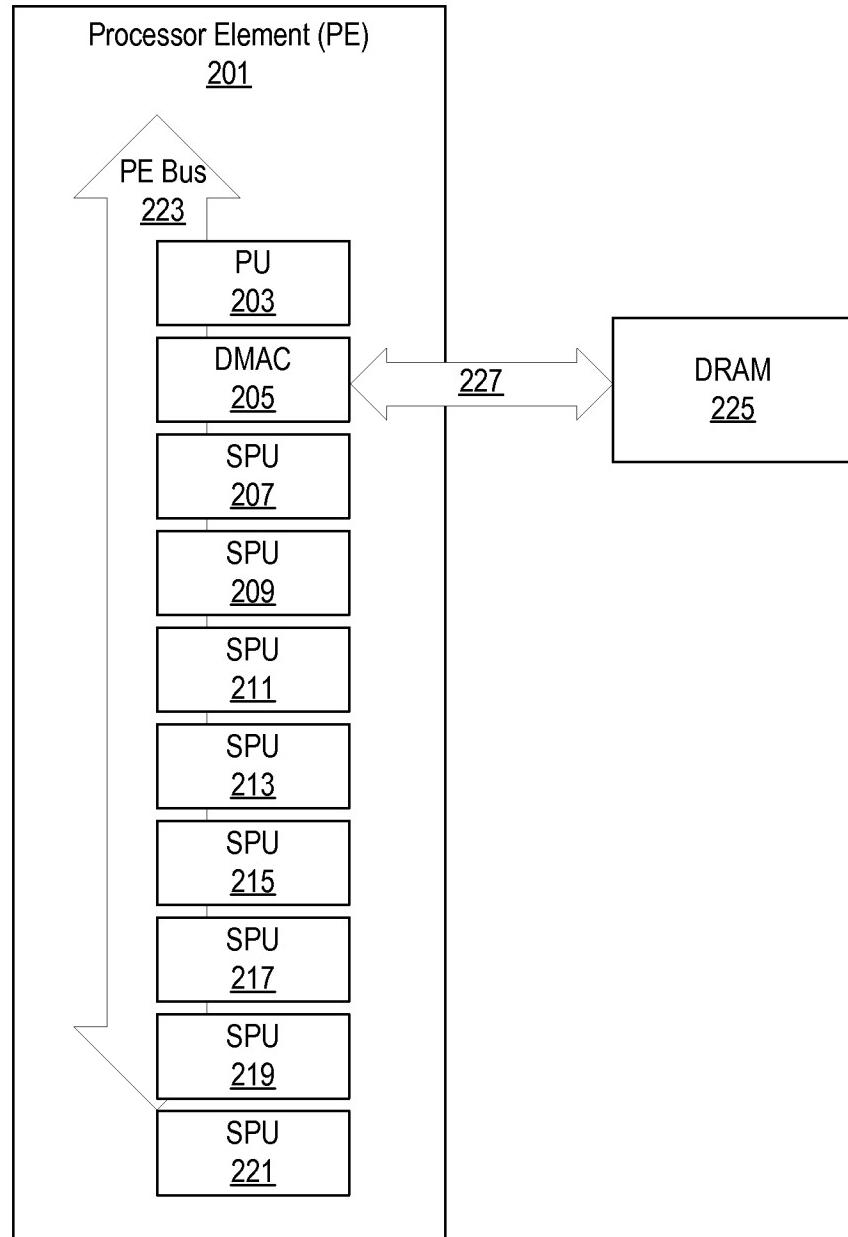


Figure 2

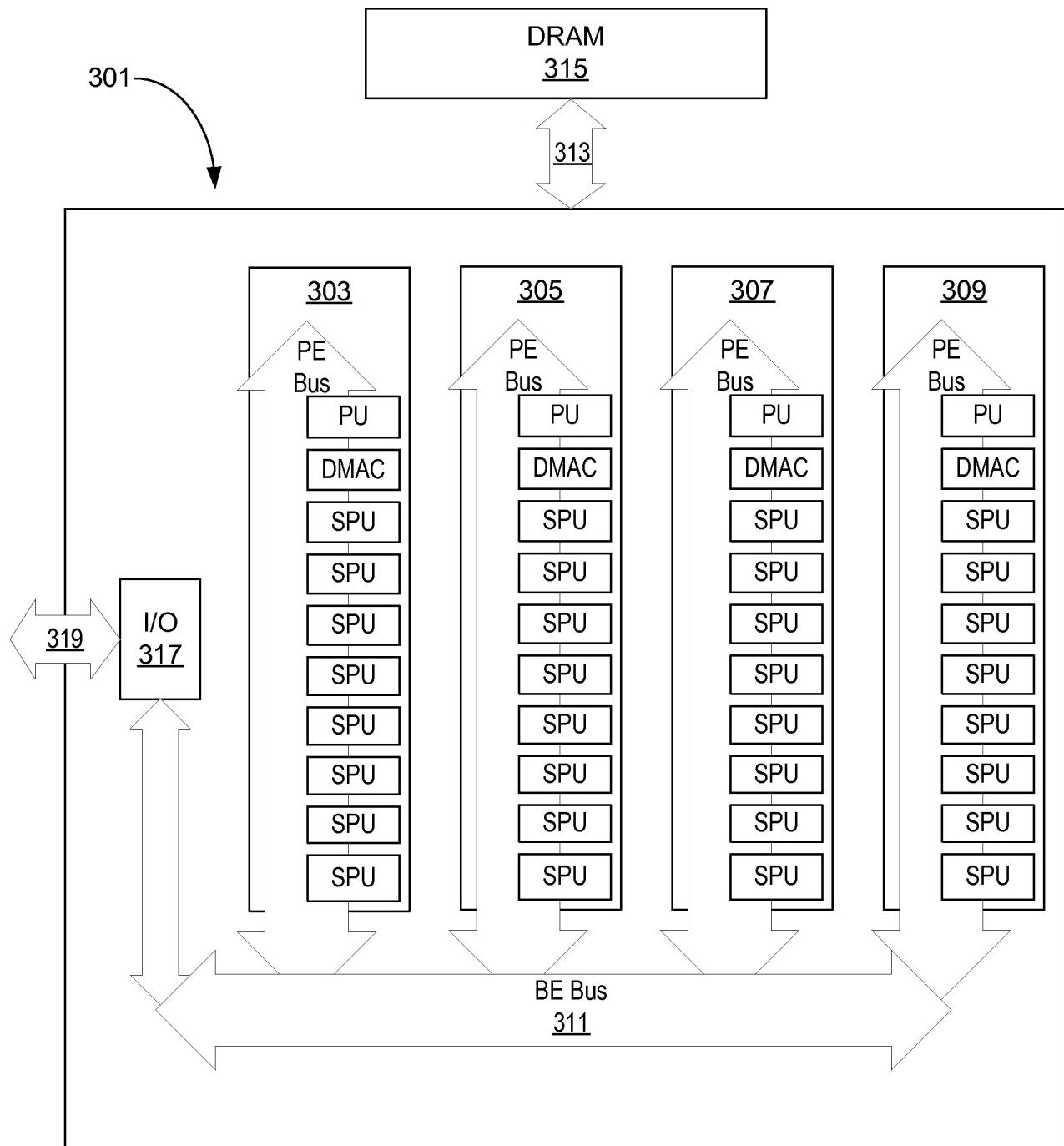


Figure 3

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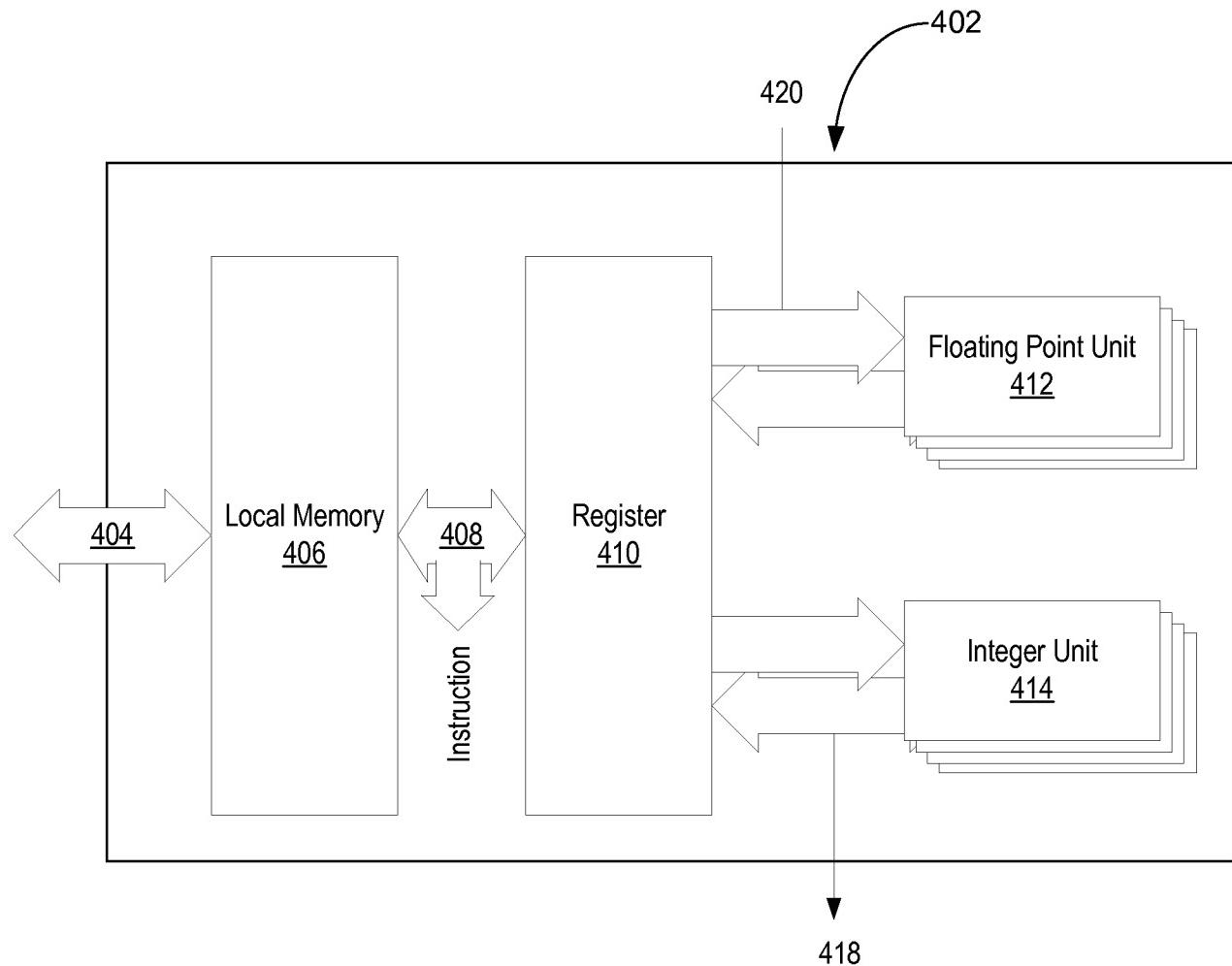


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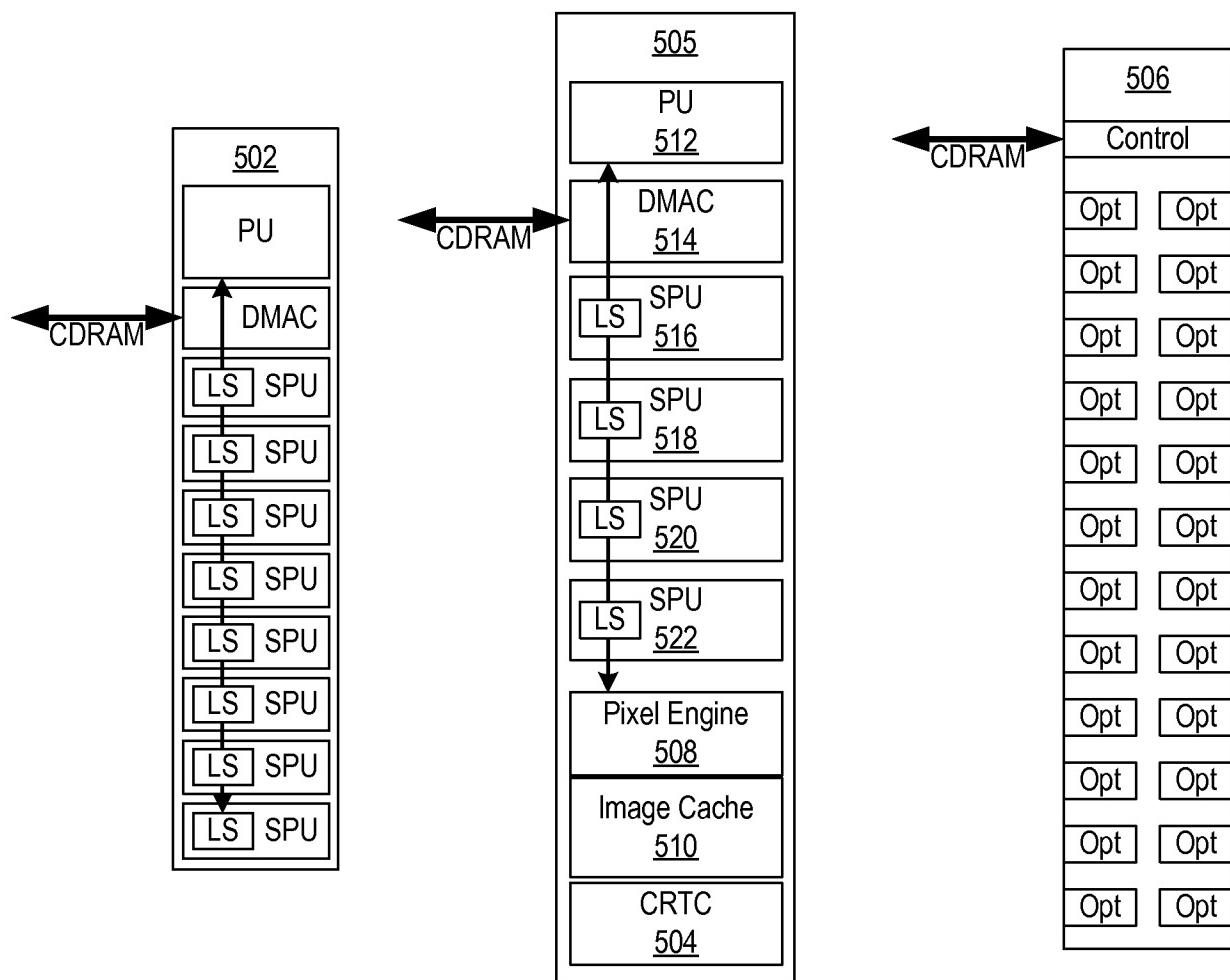
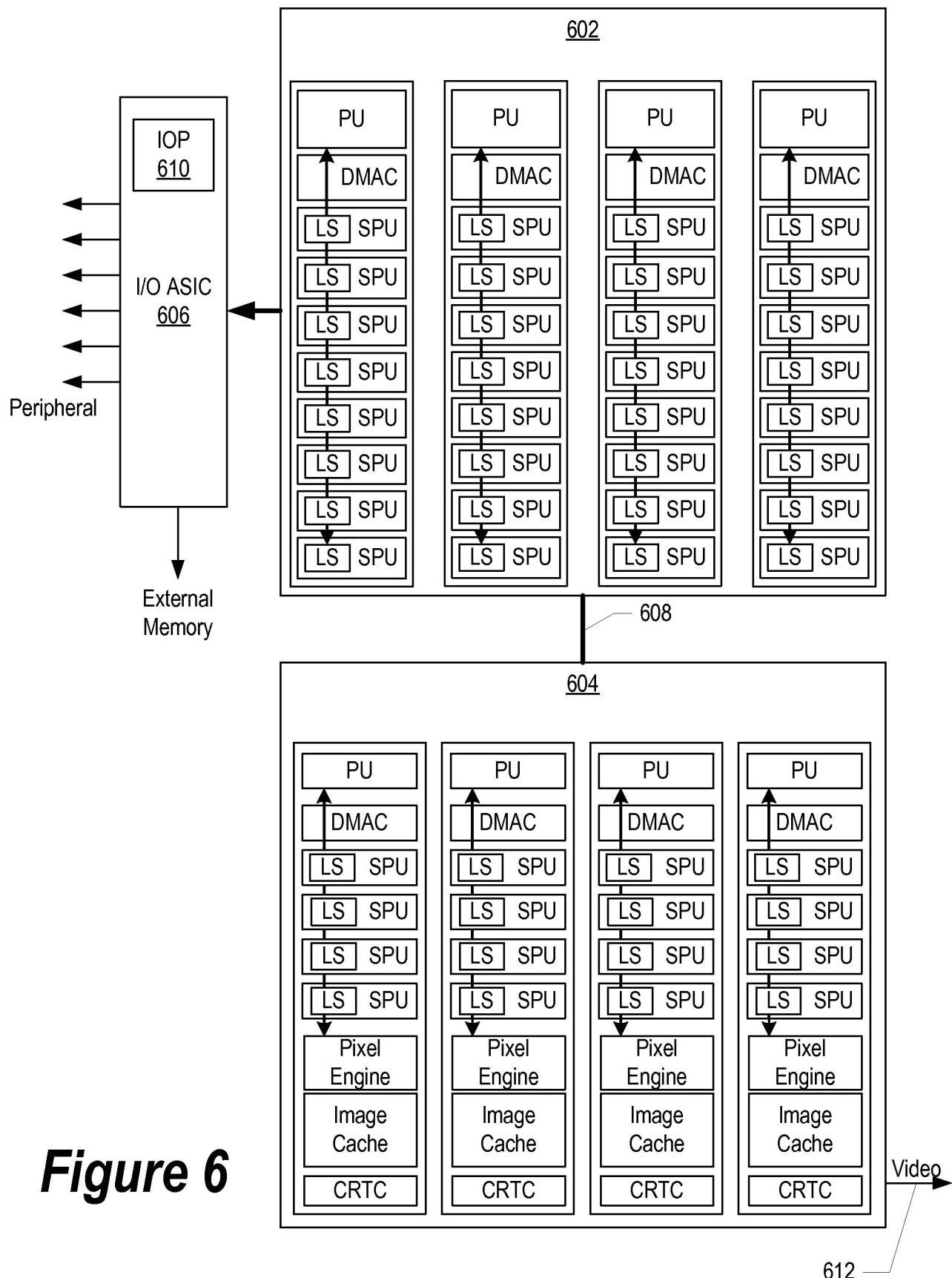
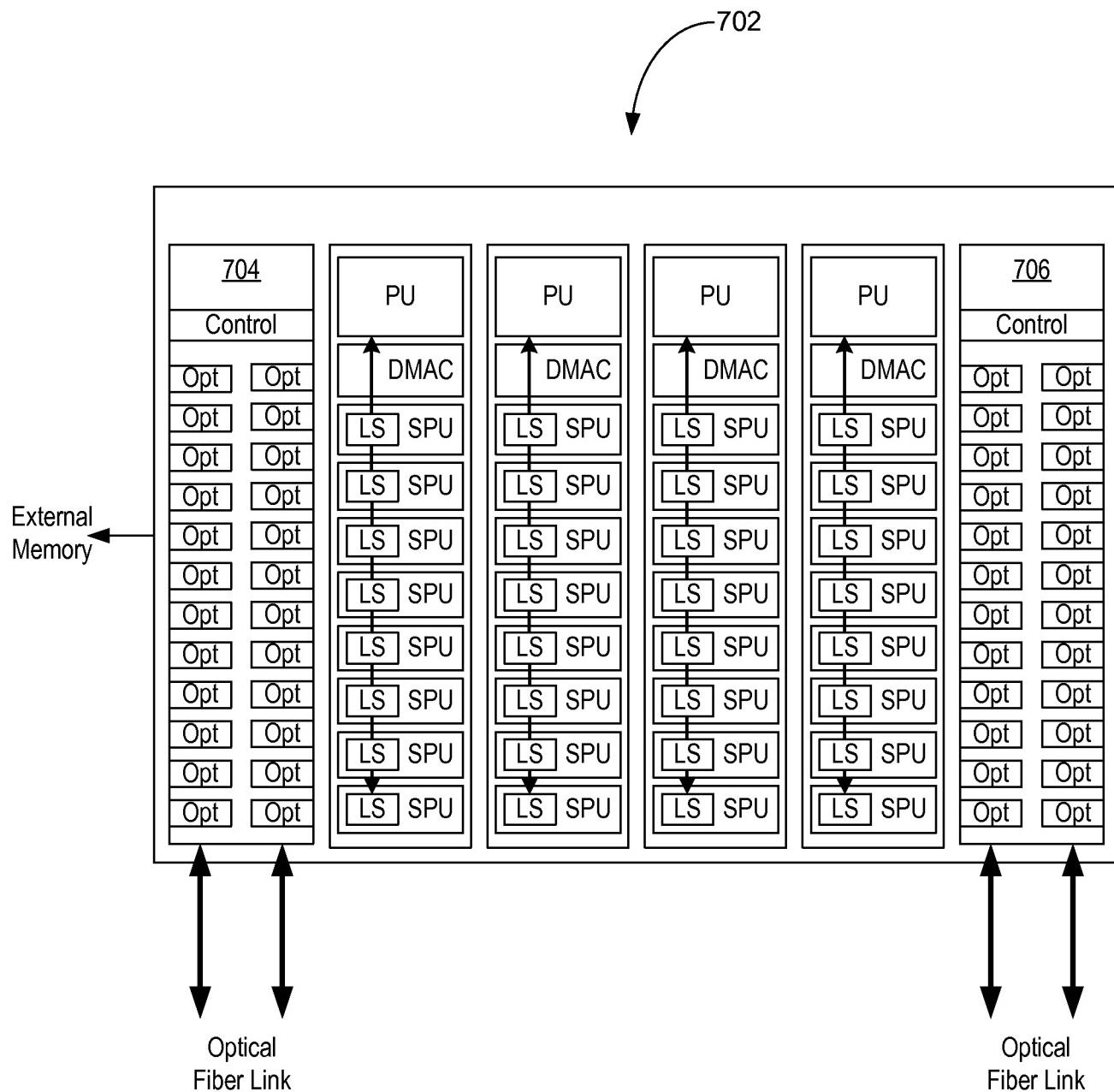


Figure 5



**Figure 7**

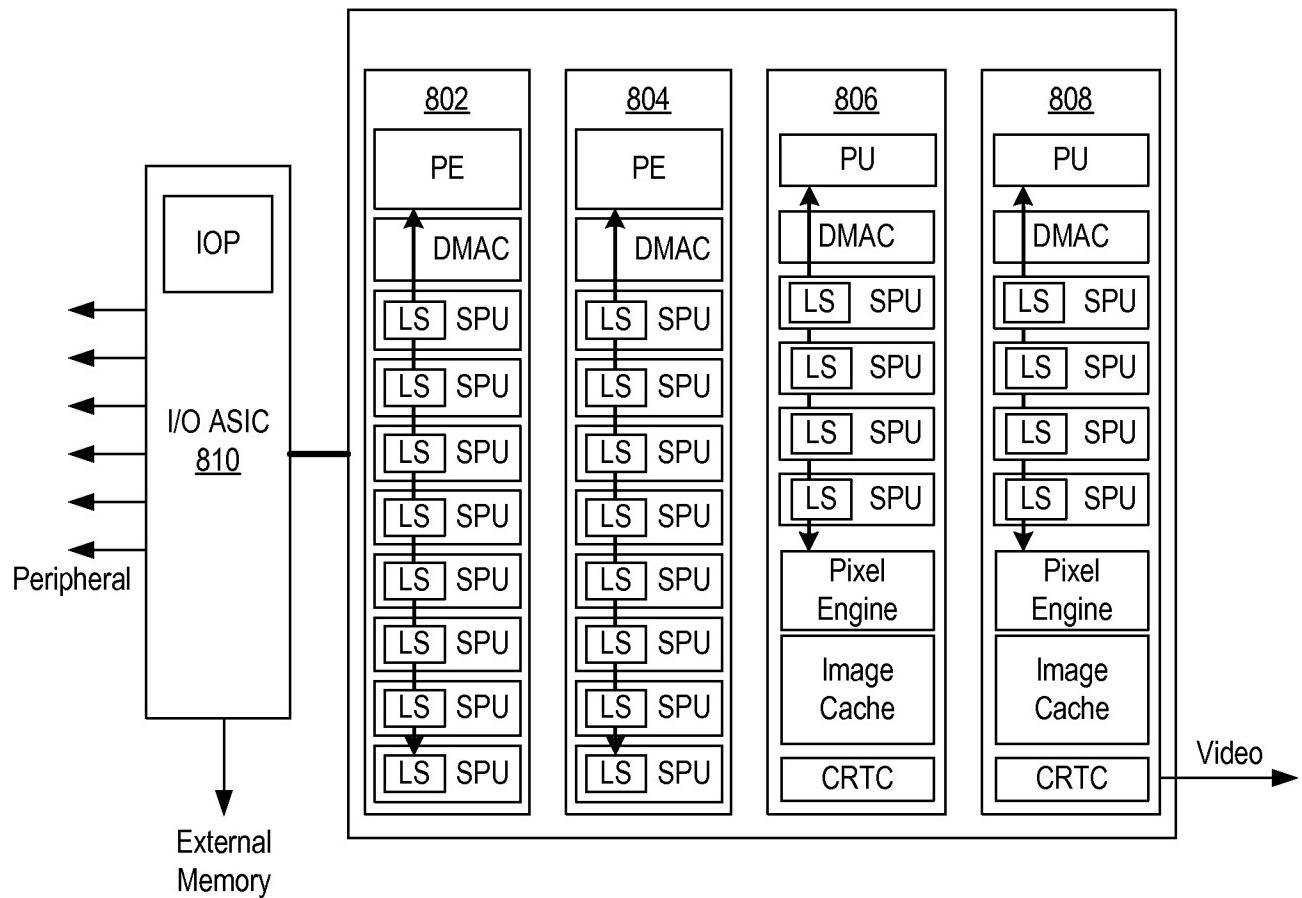


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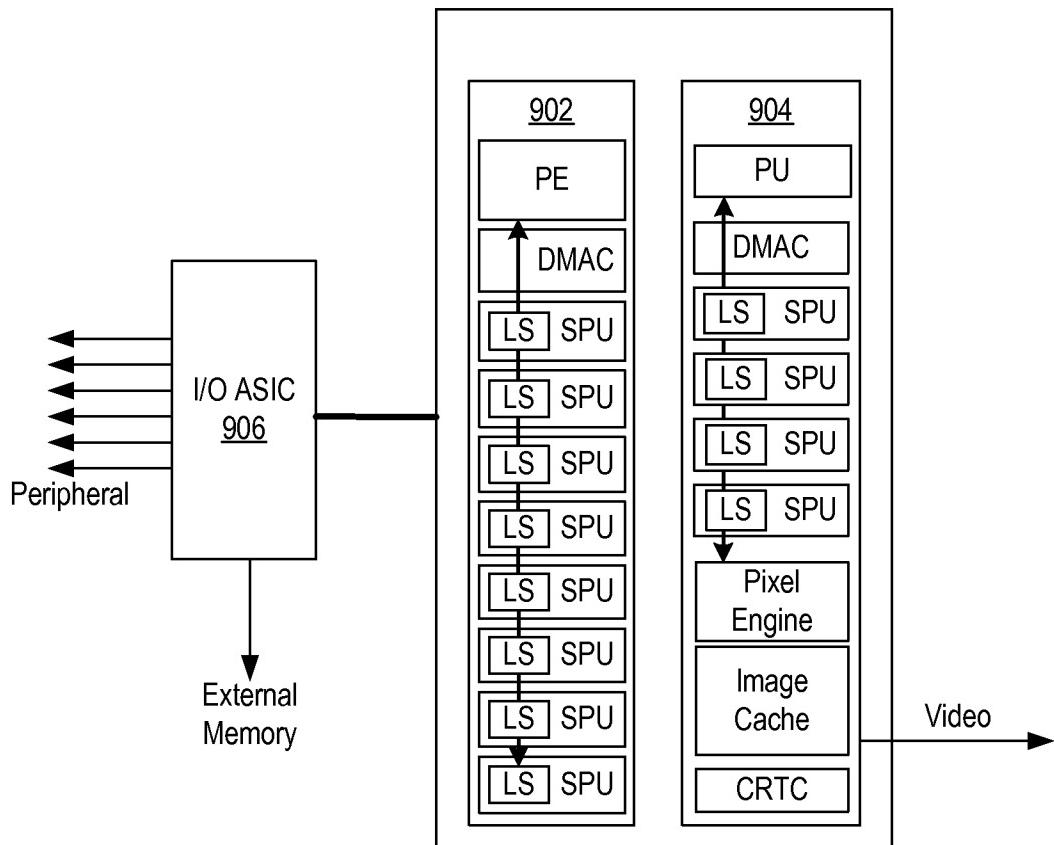


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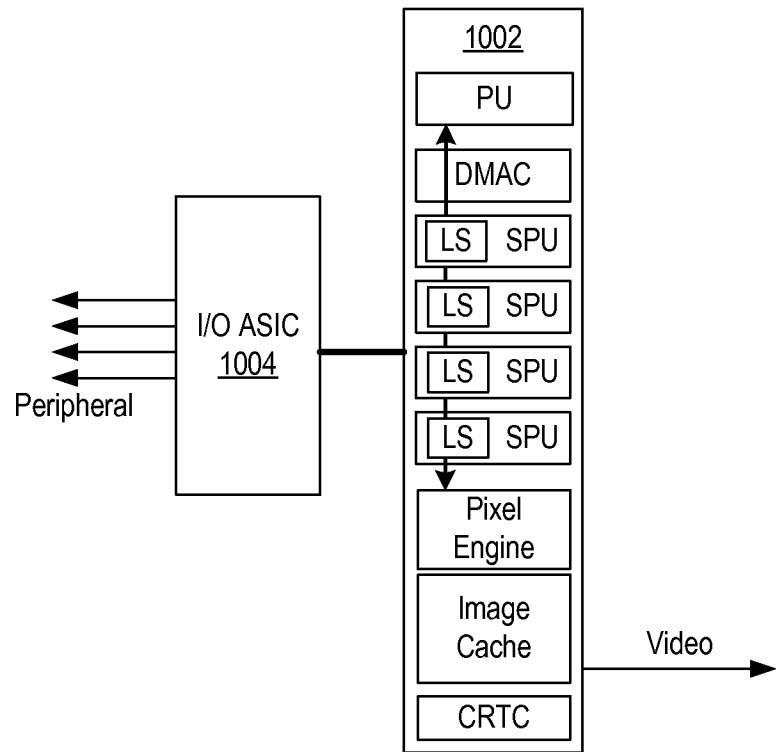
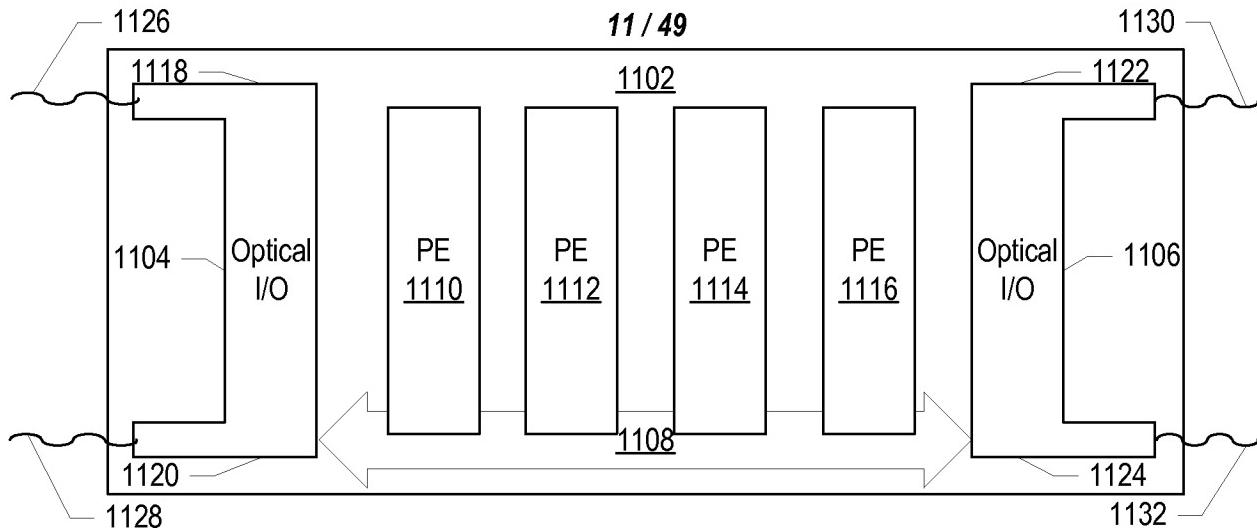
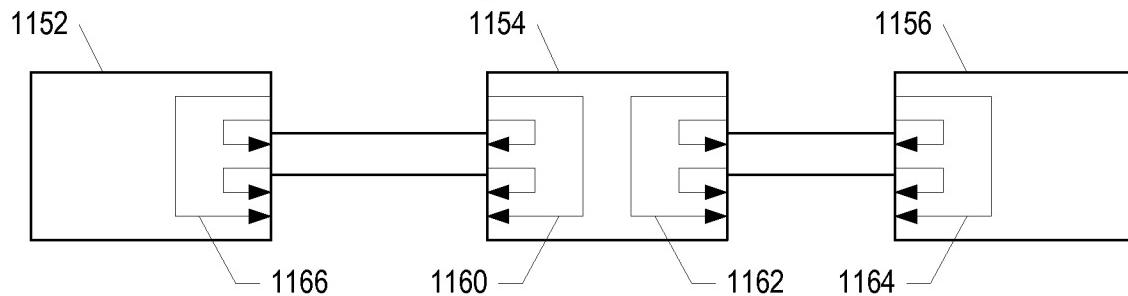
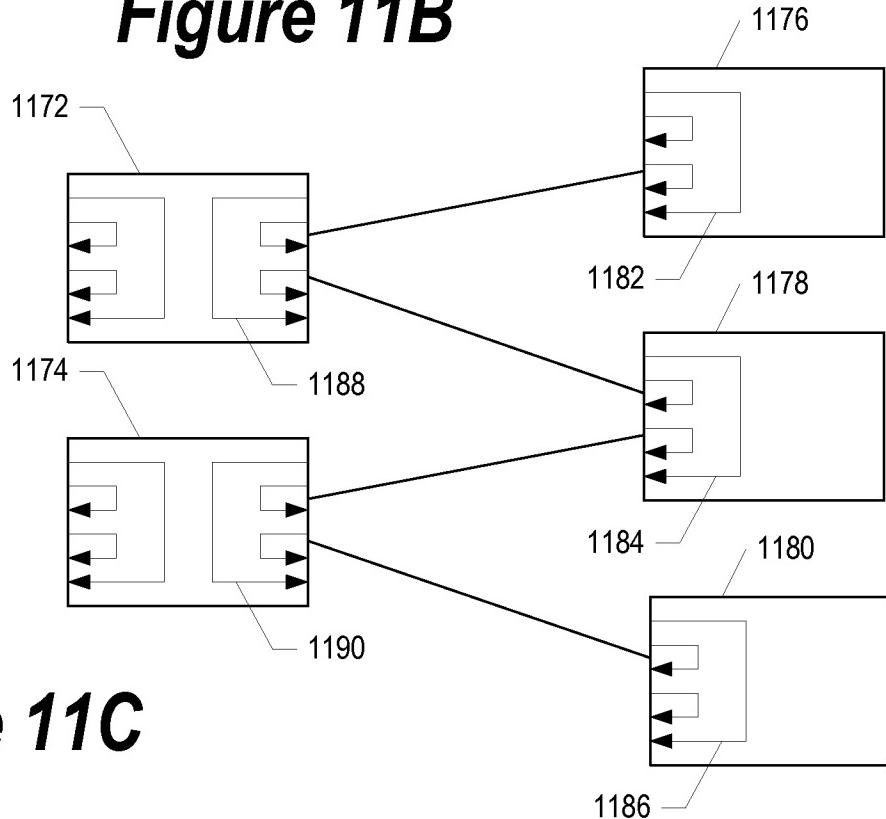
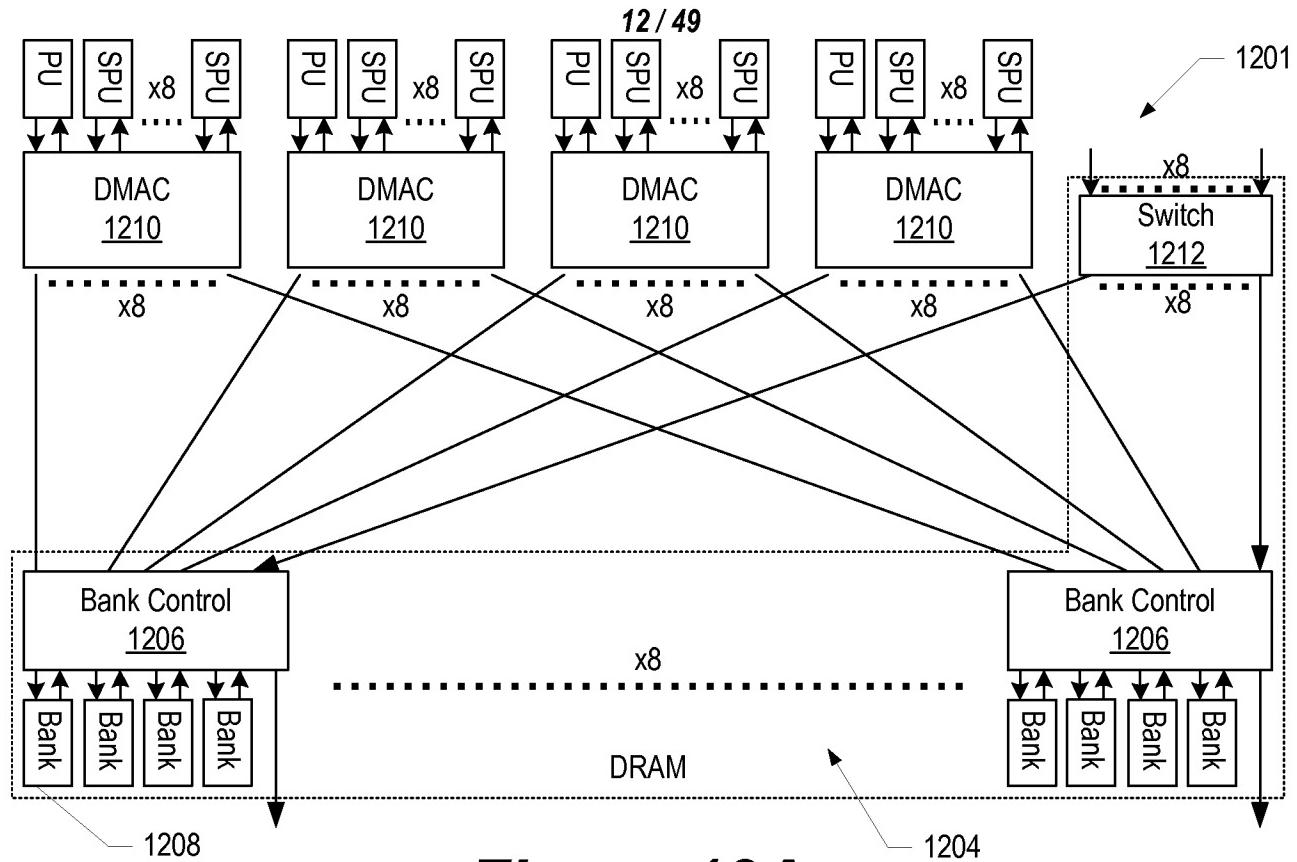
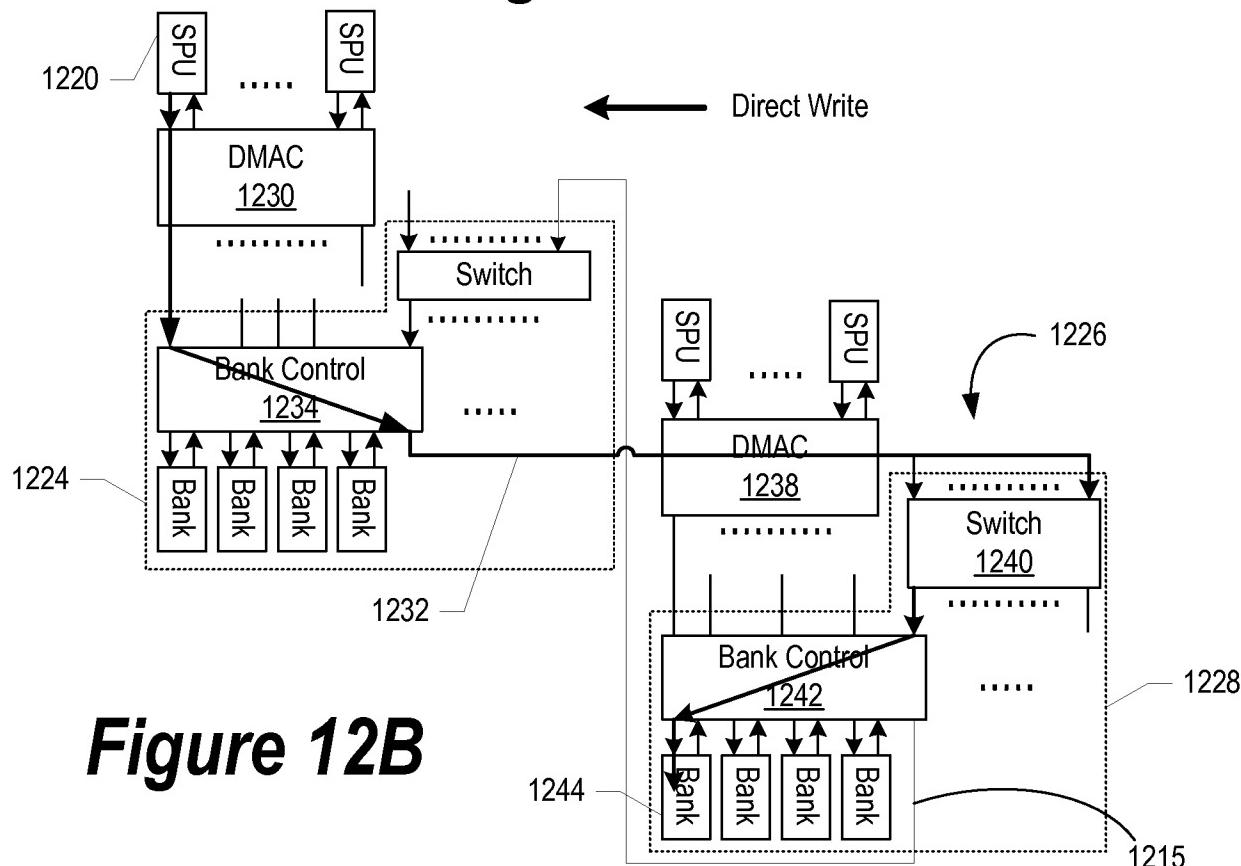


Figure 10

**Figure 11A****Figure 11B****Figure 11C**

**Figure 12A****Figure 12B**

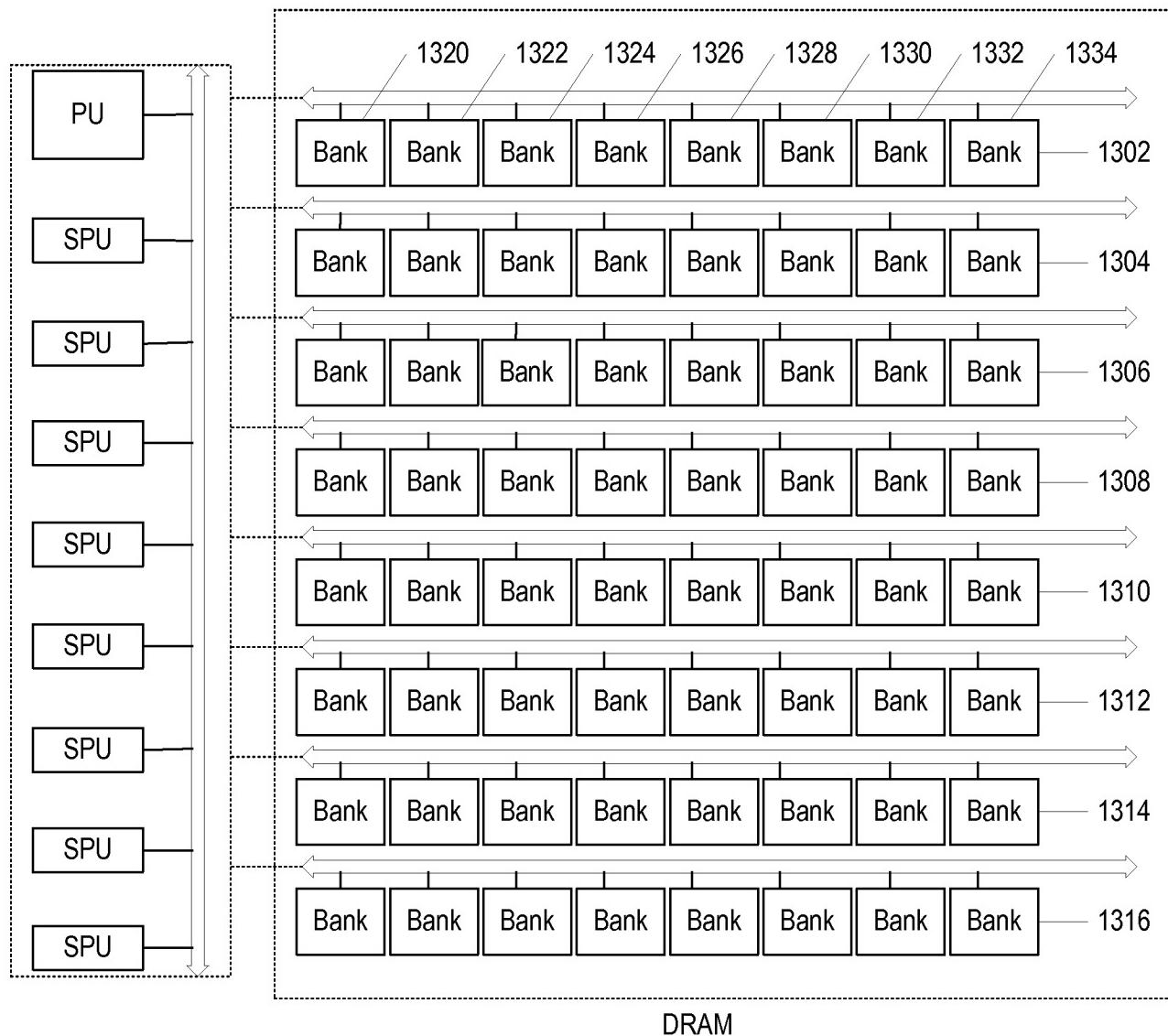


Figure 13

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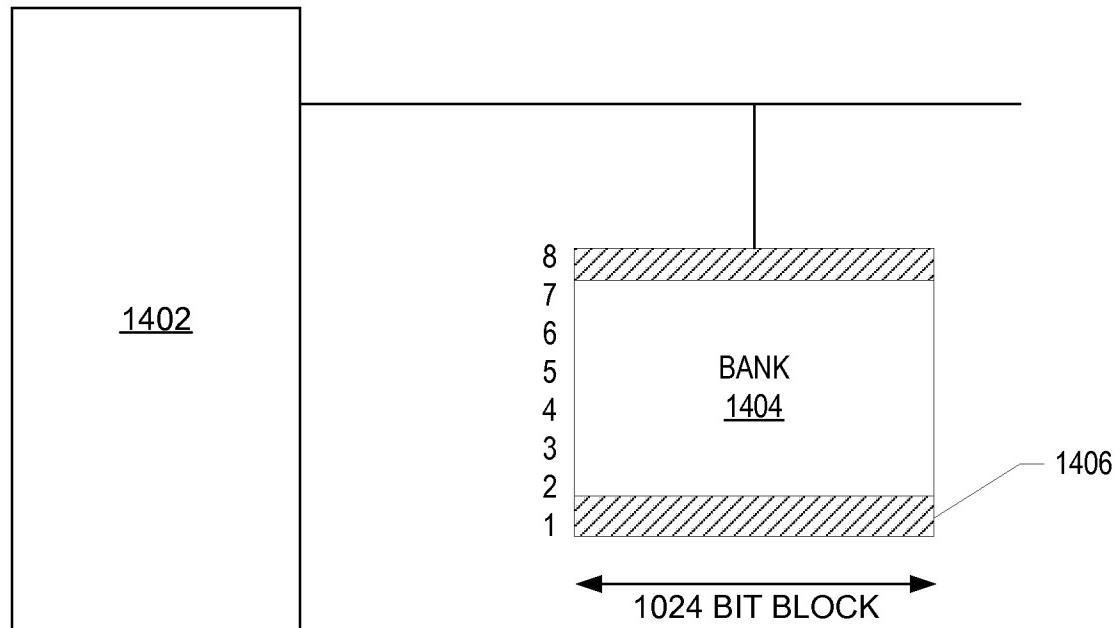


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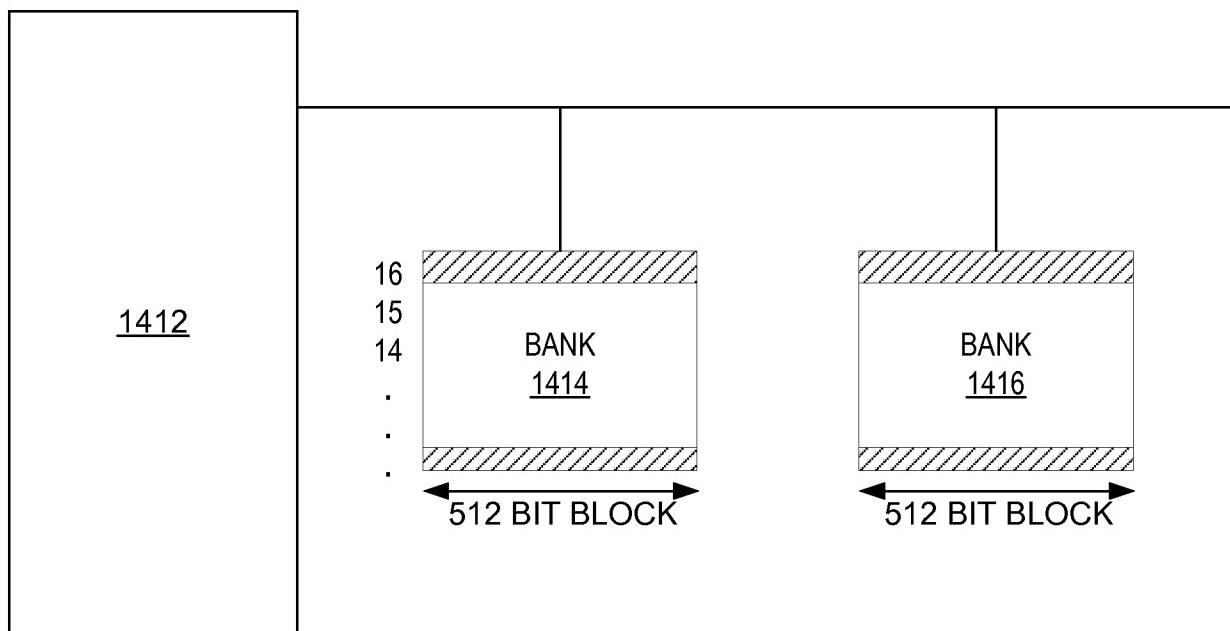


Figure 14B

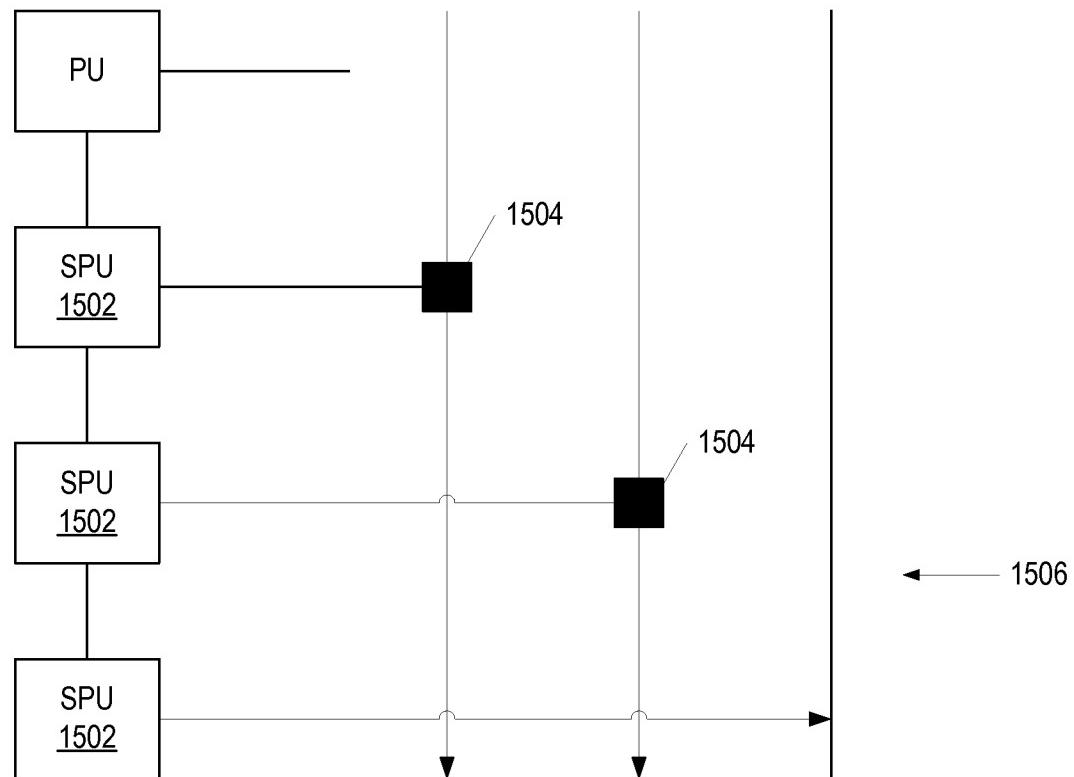


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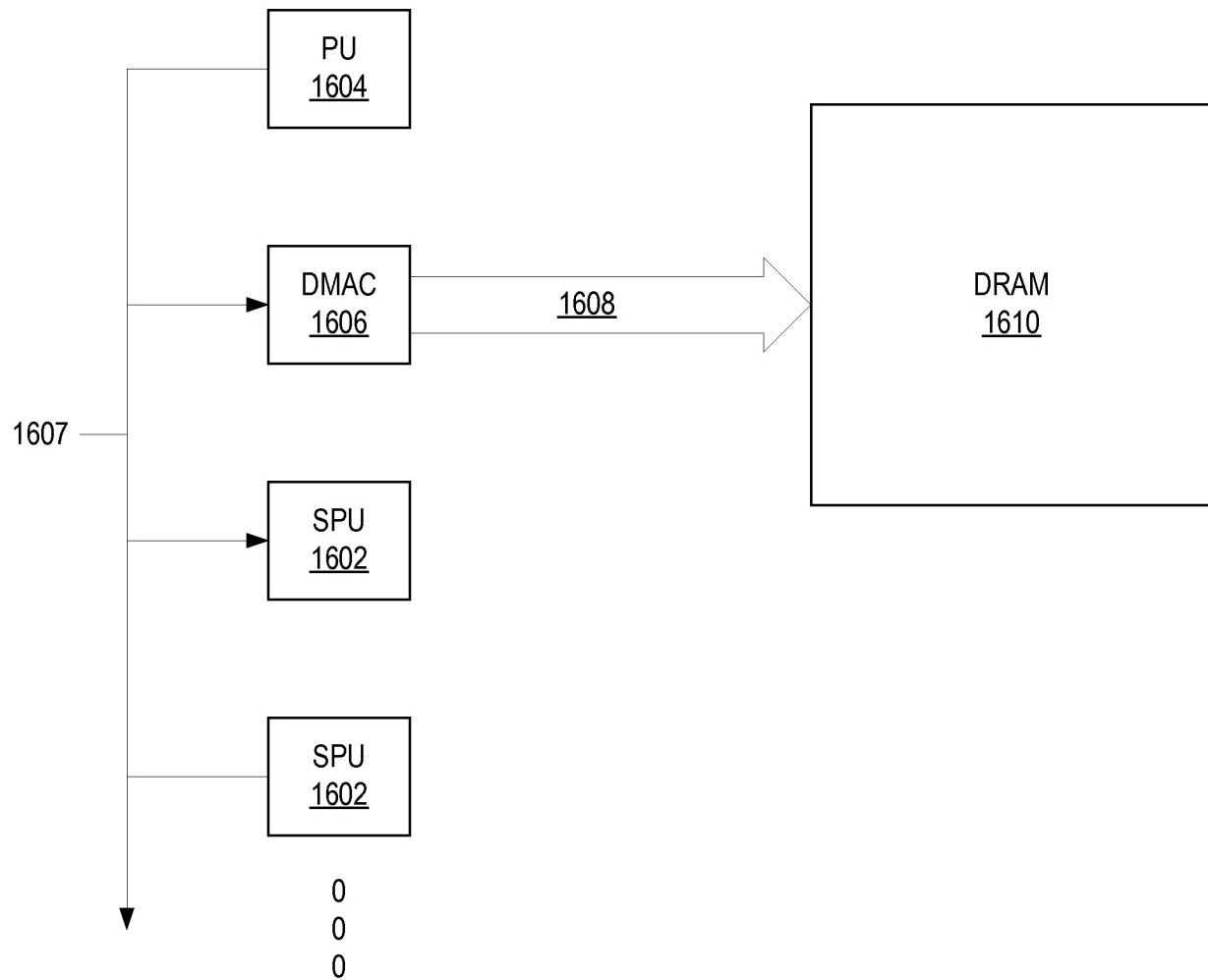
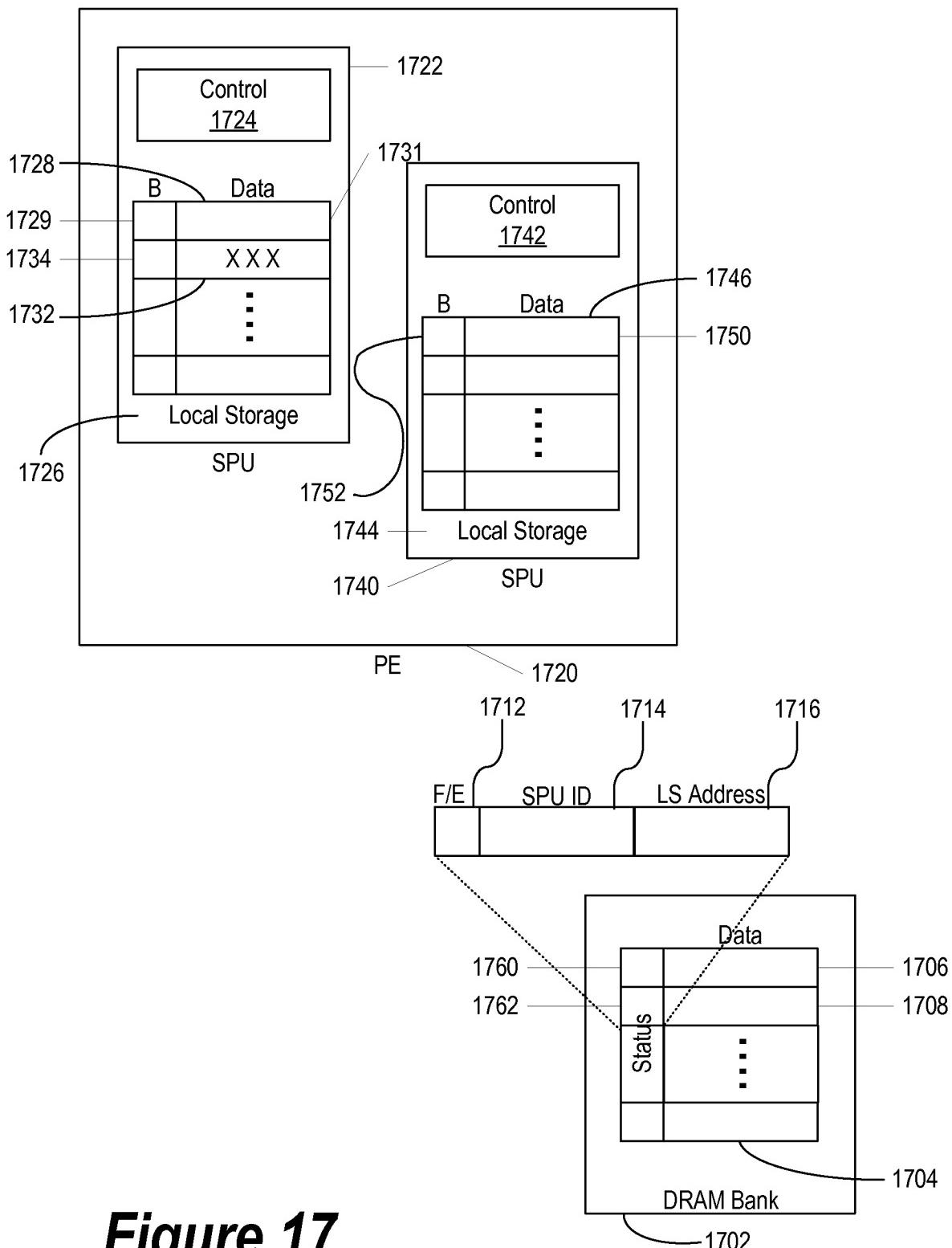


Figure 16

**Figure 17**

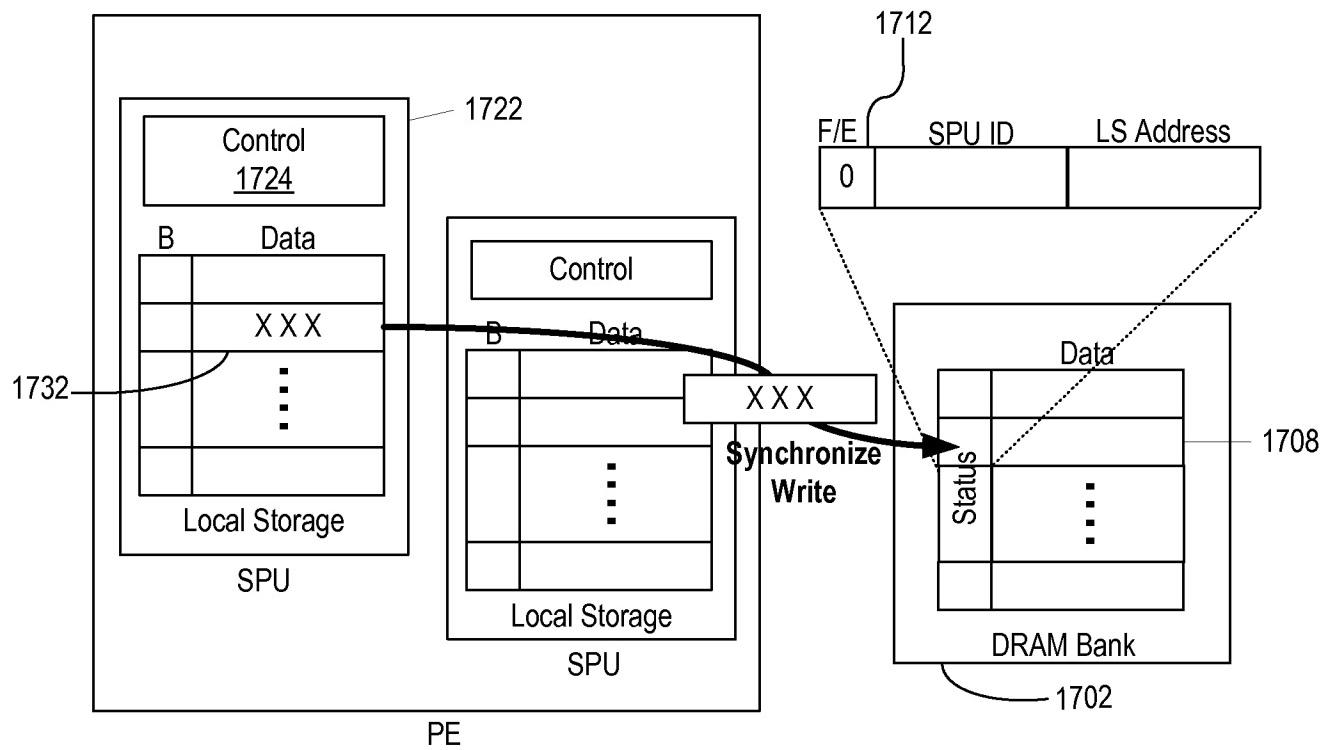


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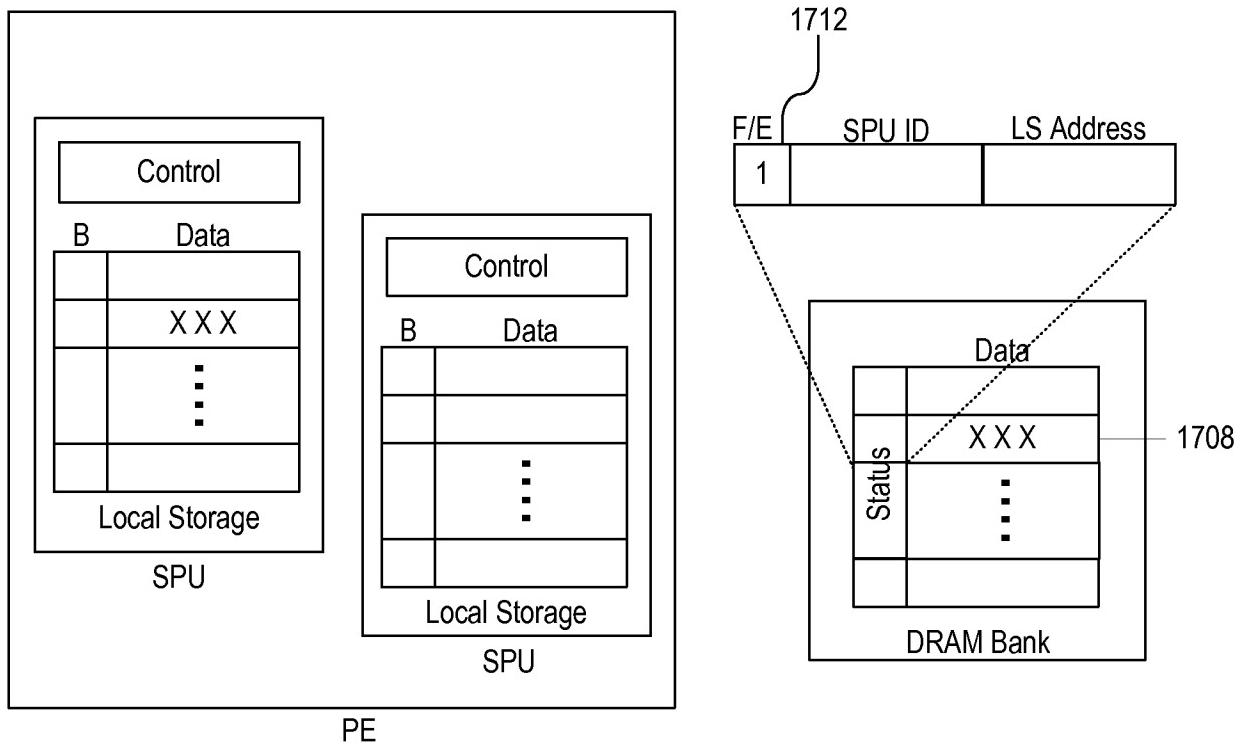
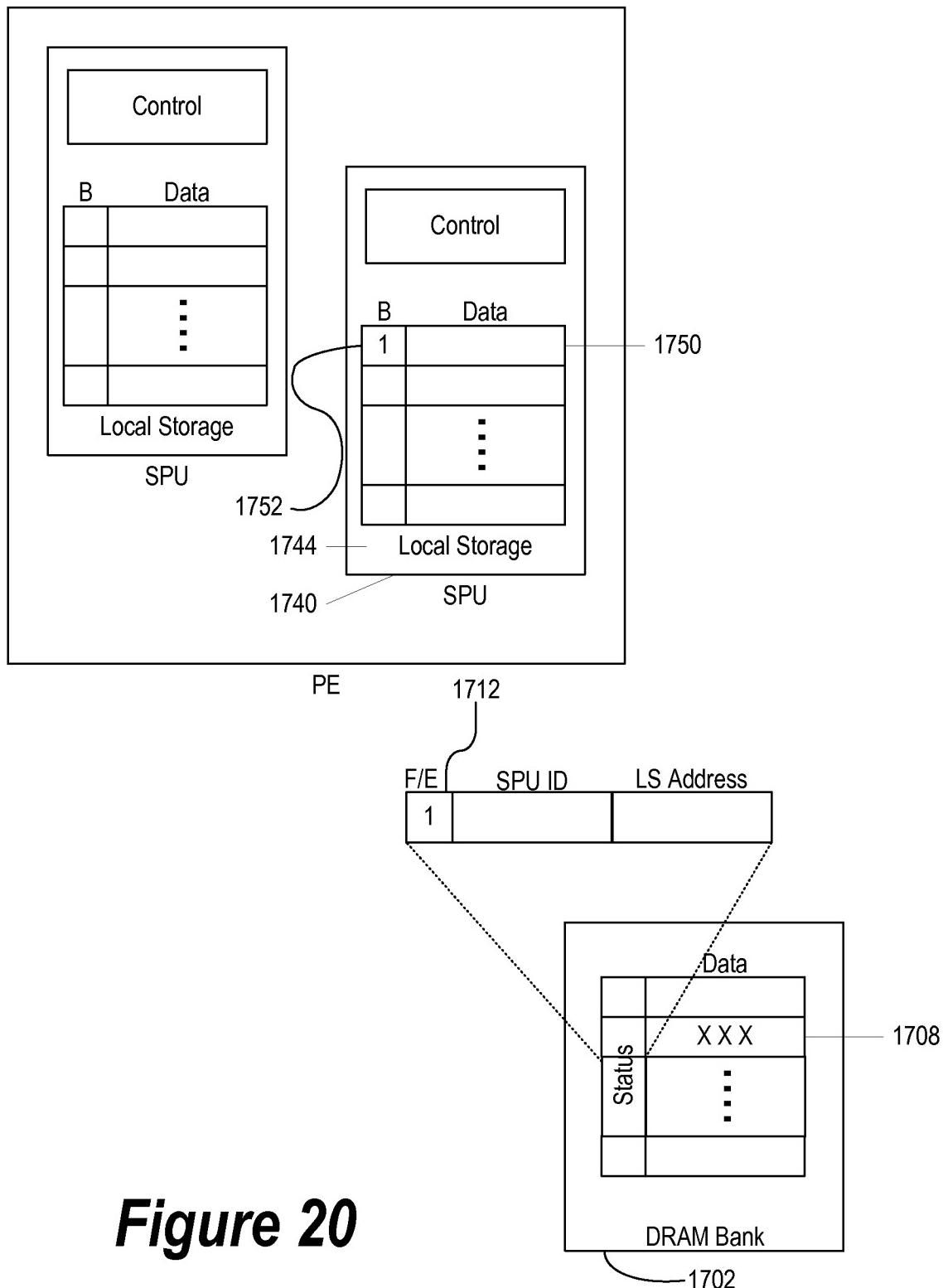


Figure 19

**Figure 20**

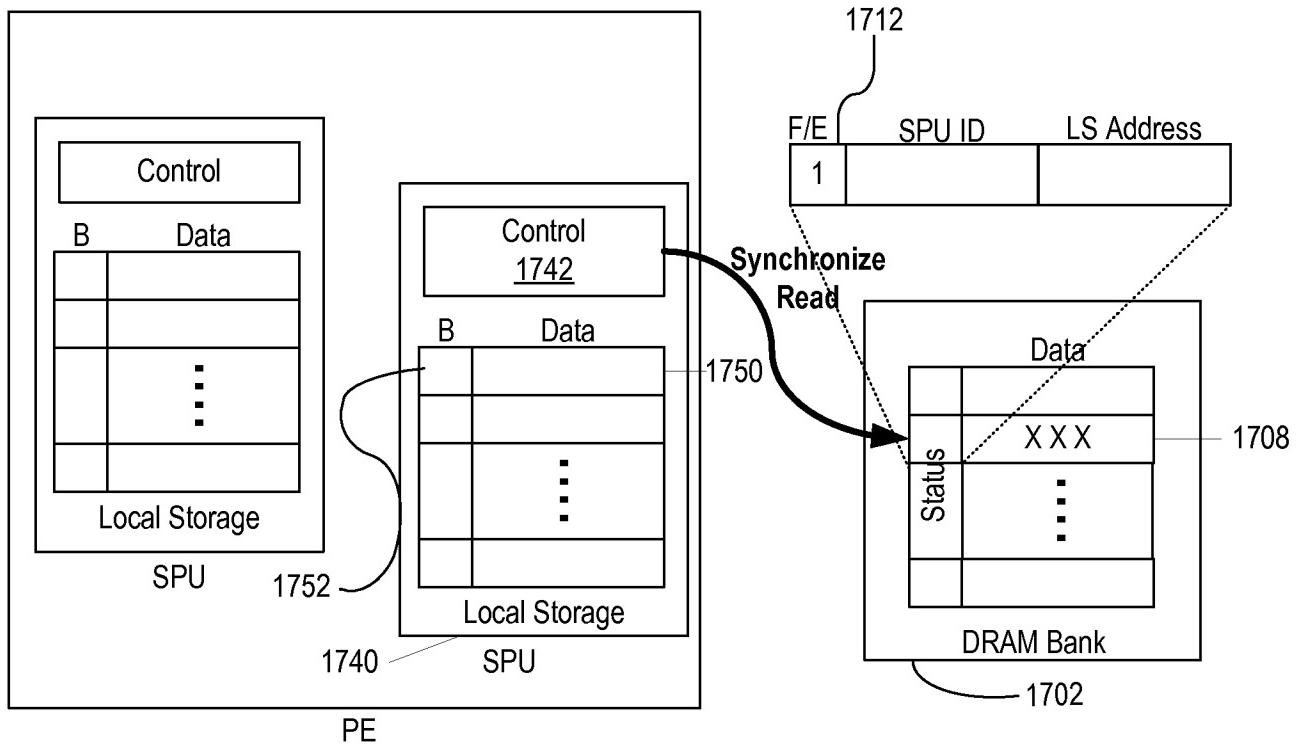
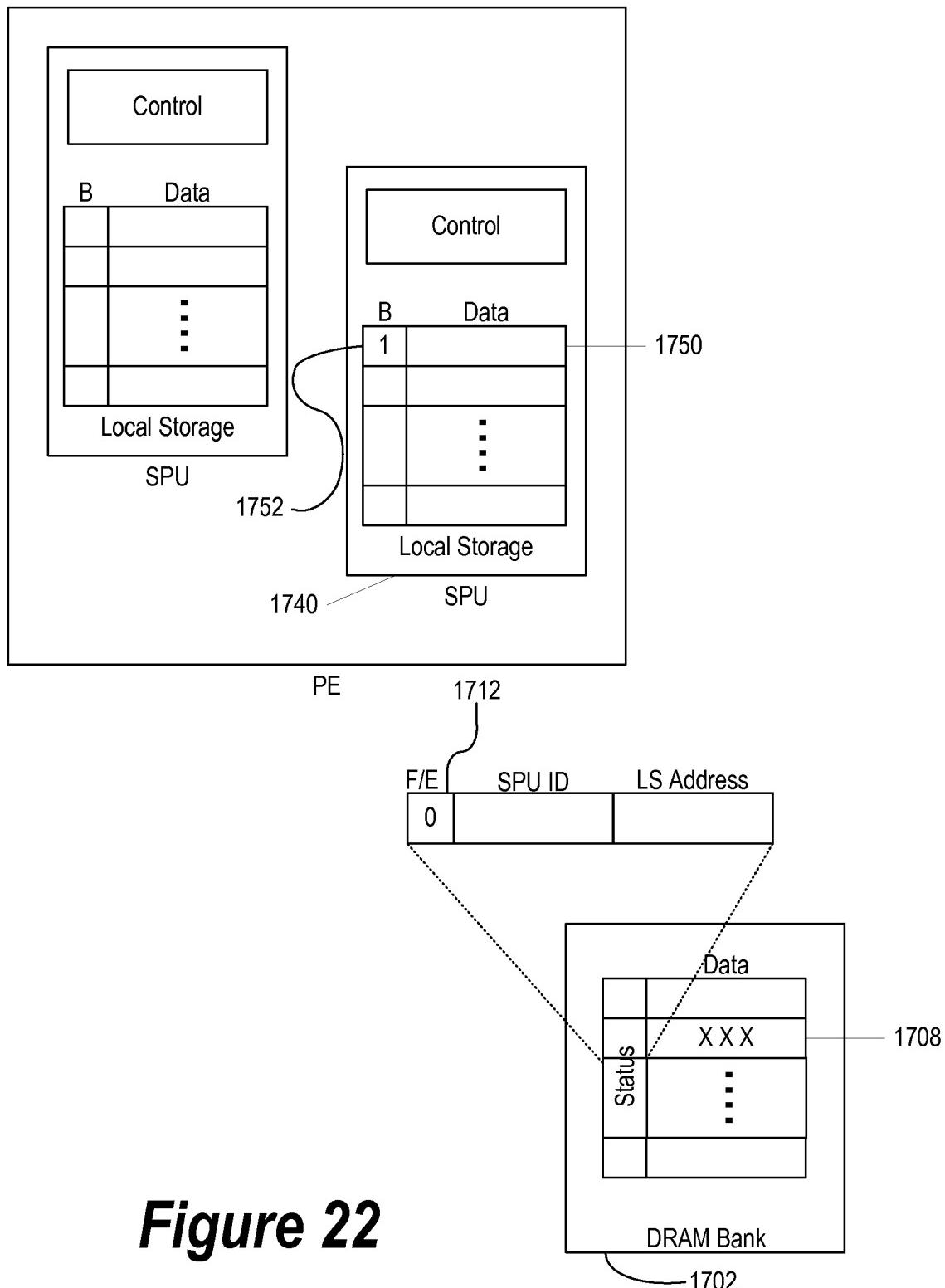


Figure 21

**Figure 22**

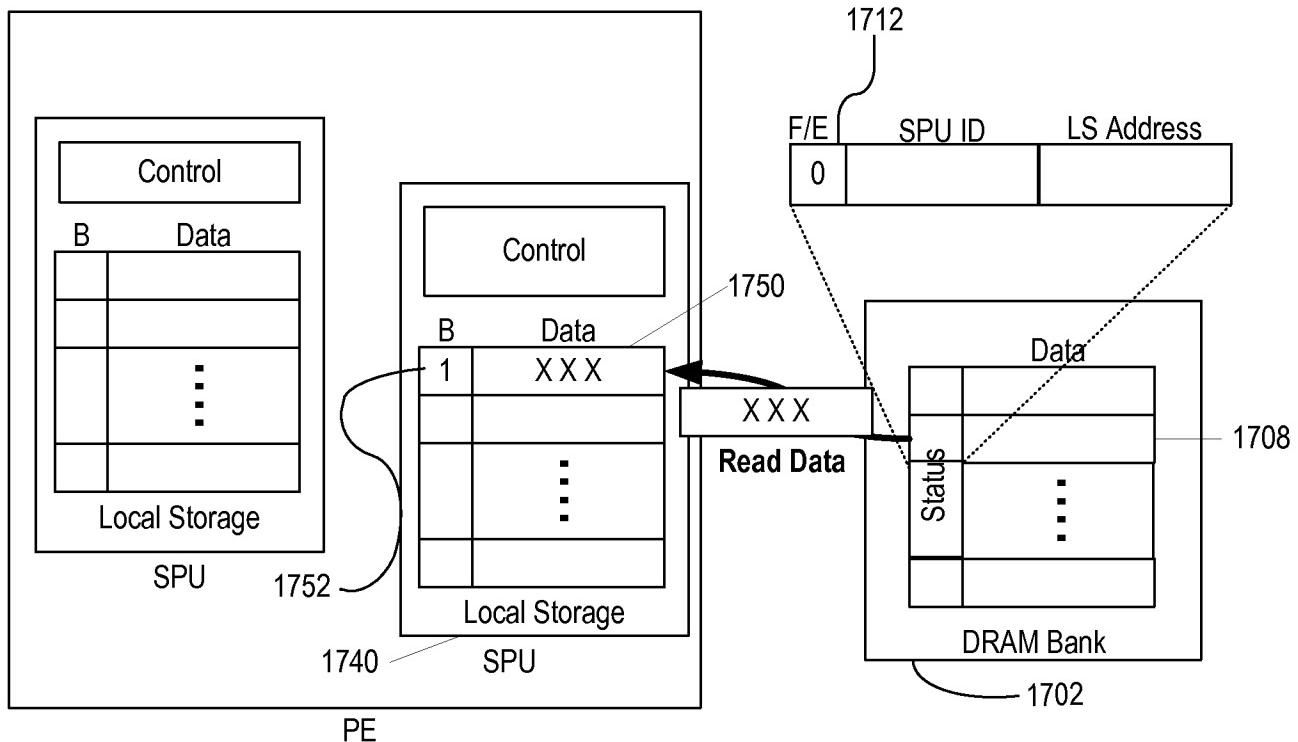
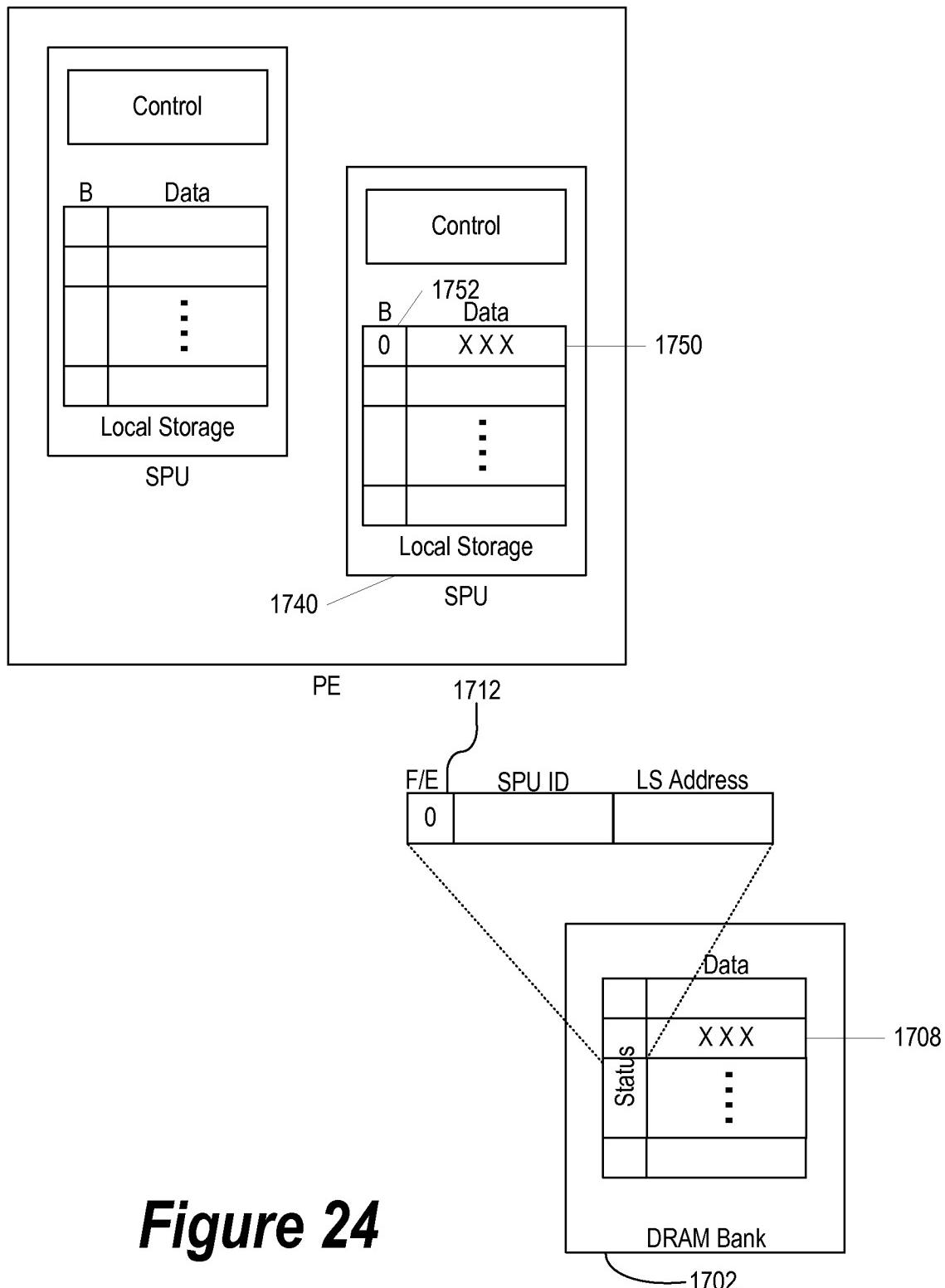


Figure 23

**Figure 24**

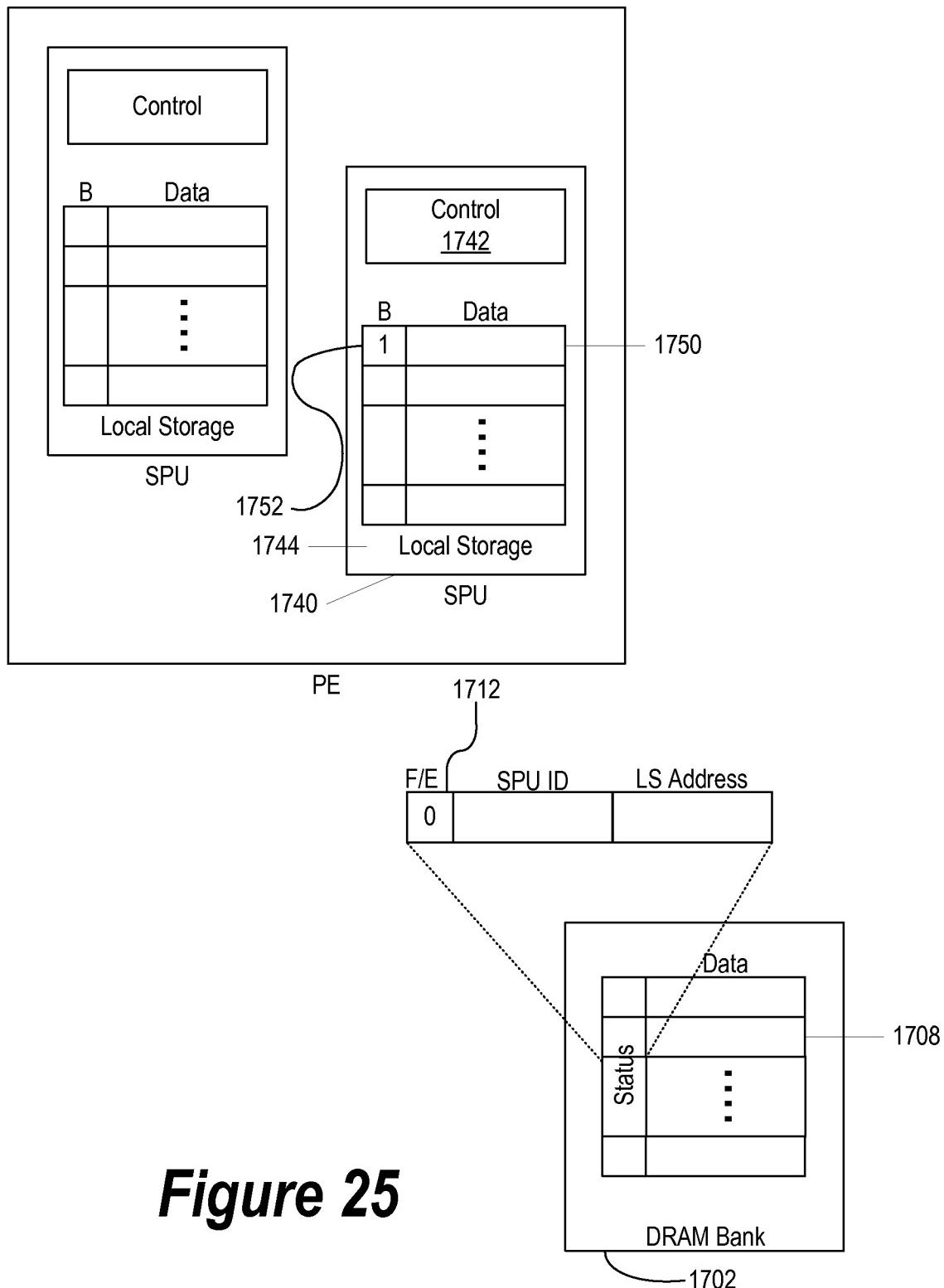


Figure 25

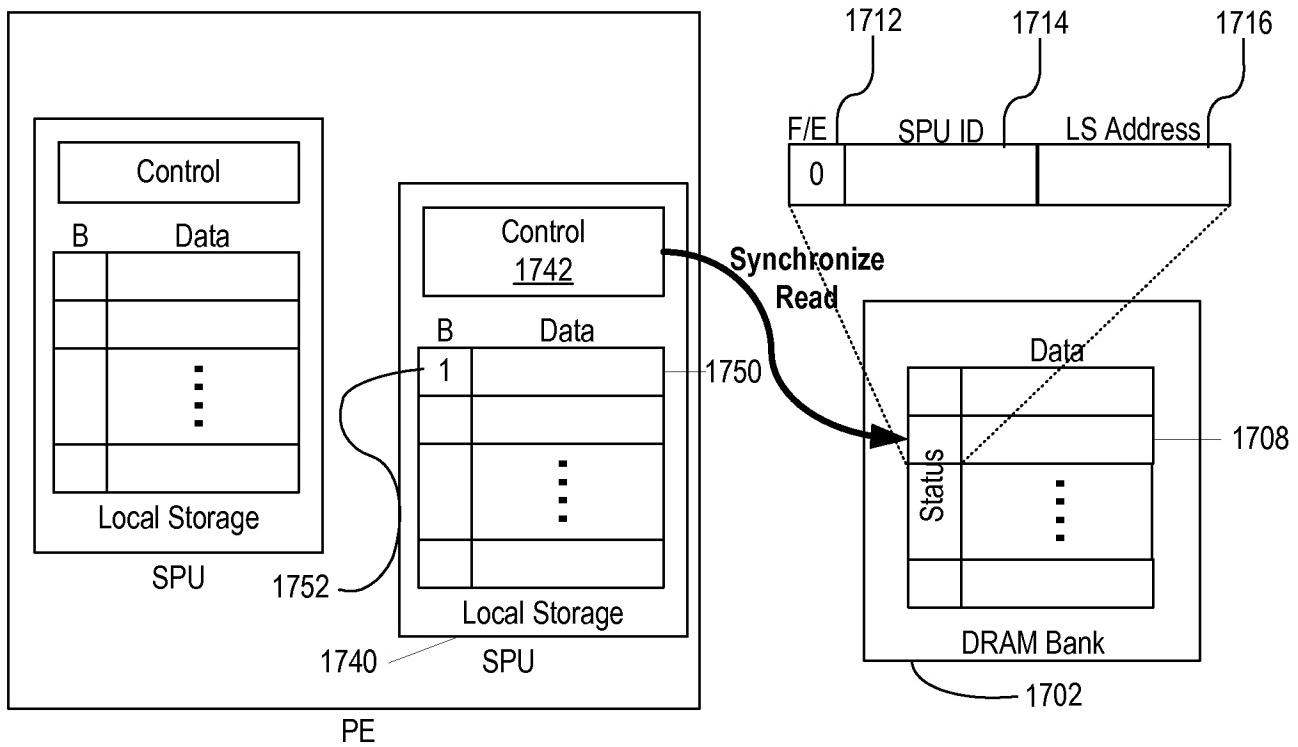


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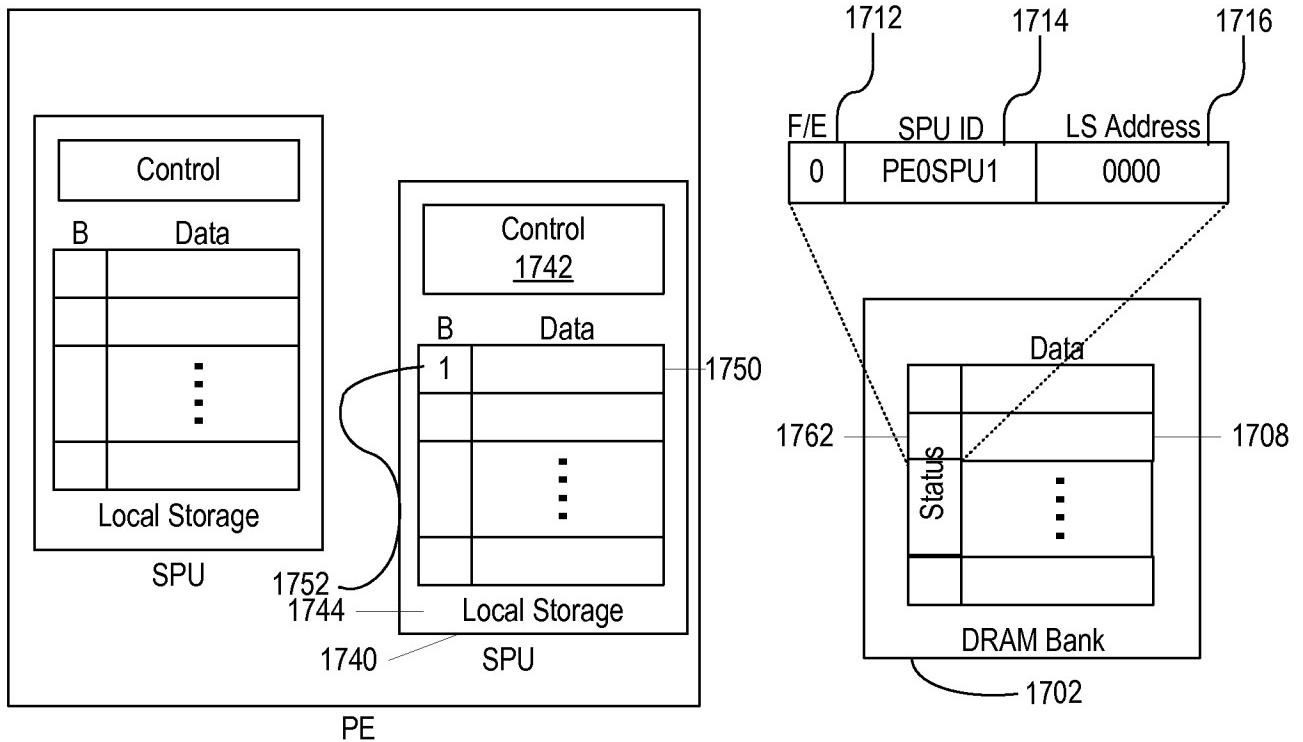


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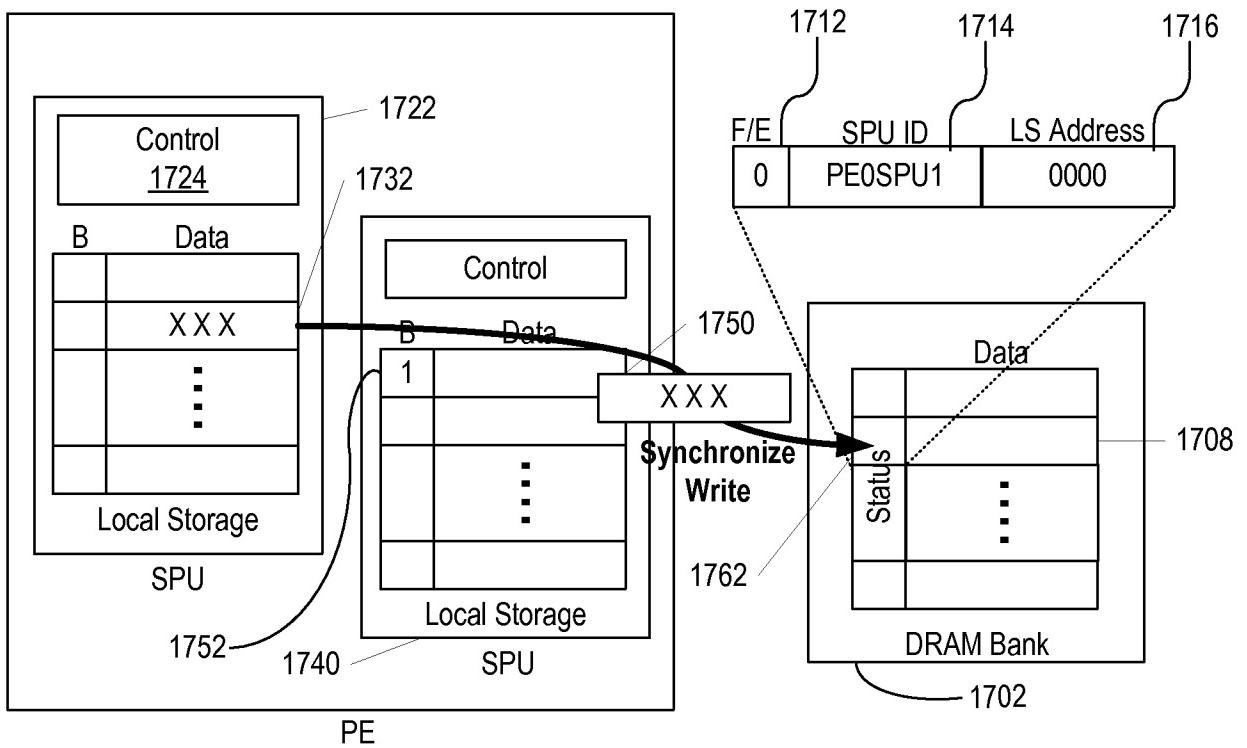


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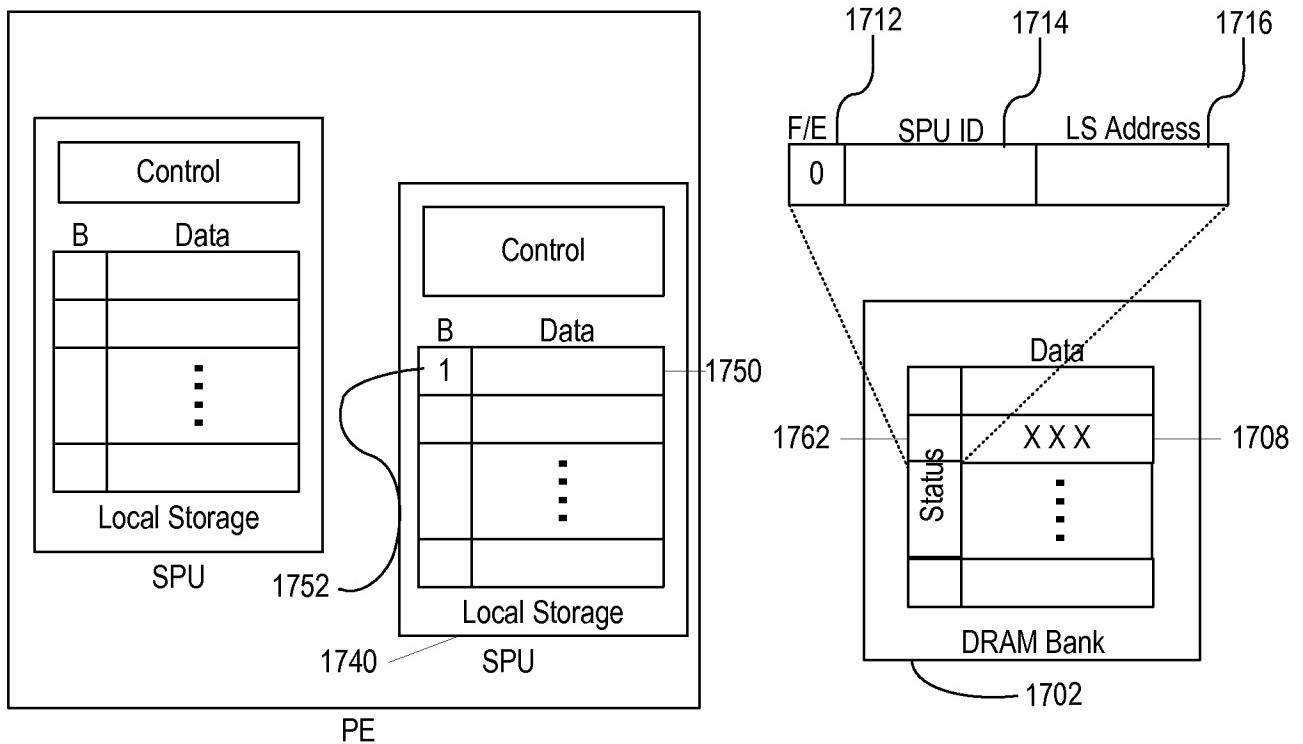
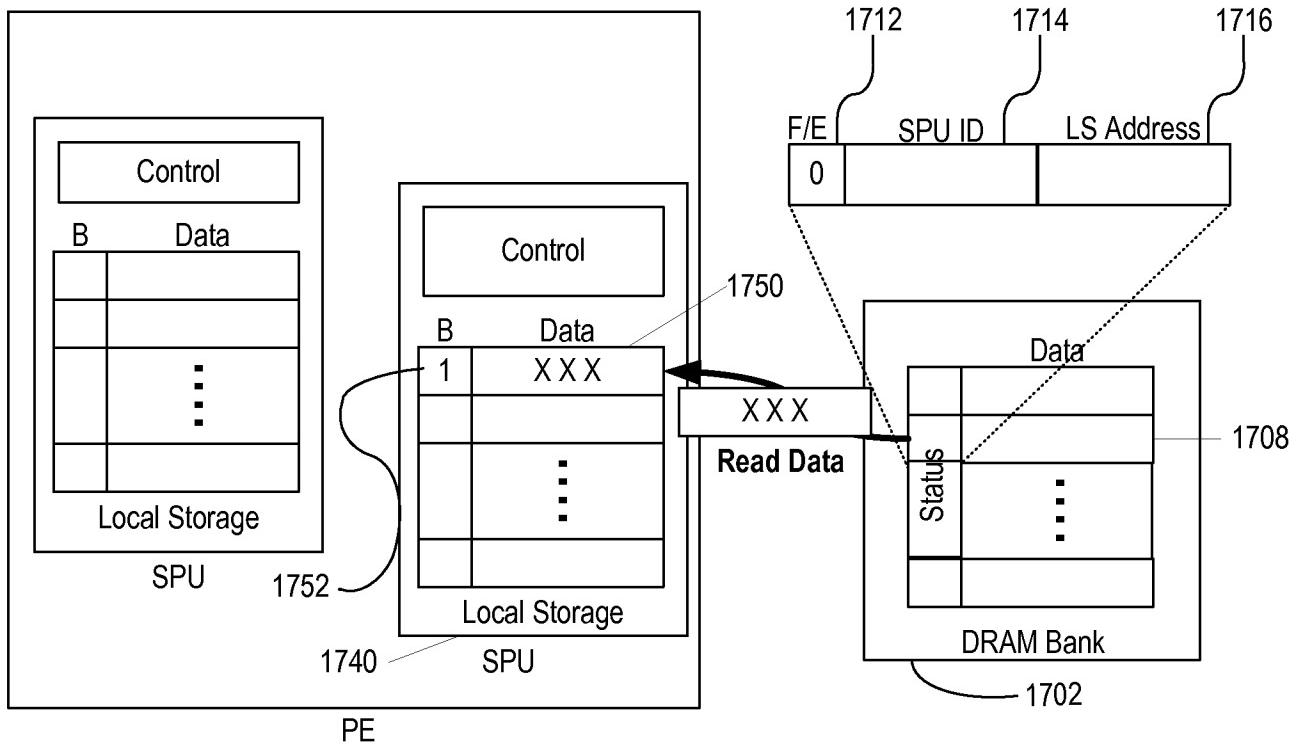


Figure 29

*Figure 30*

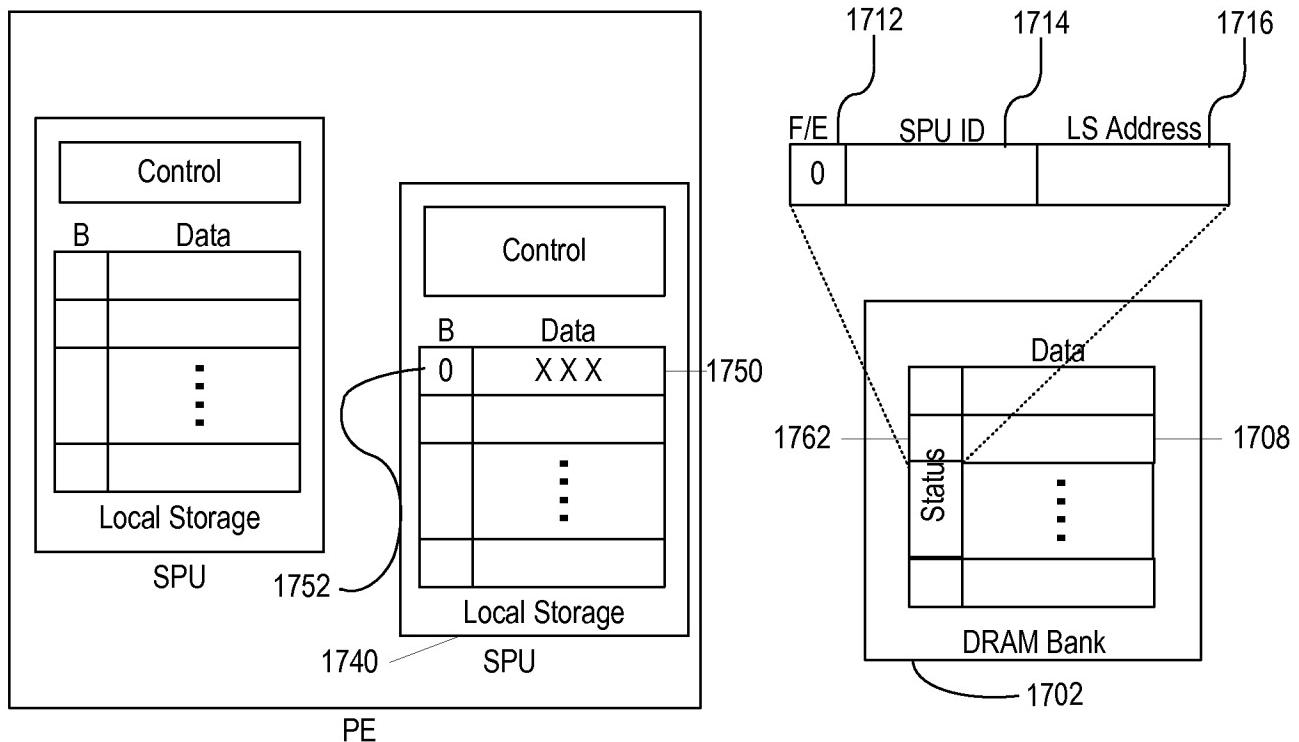


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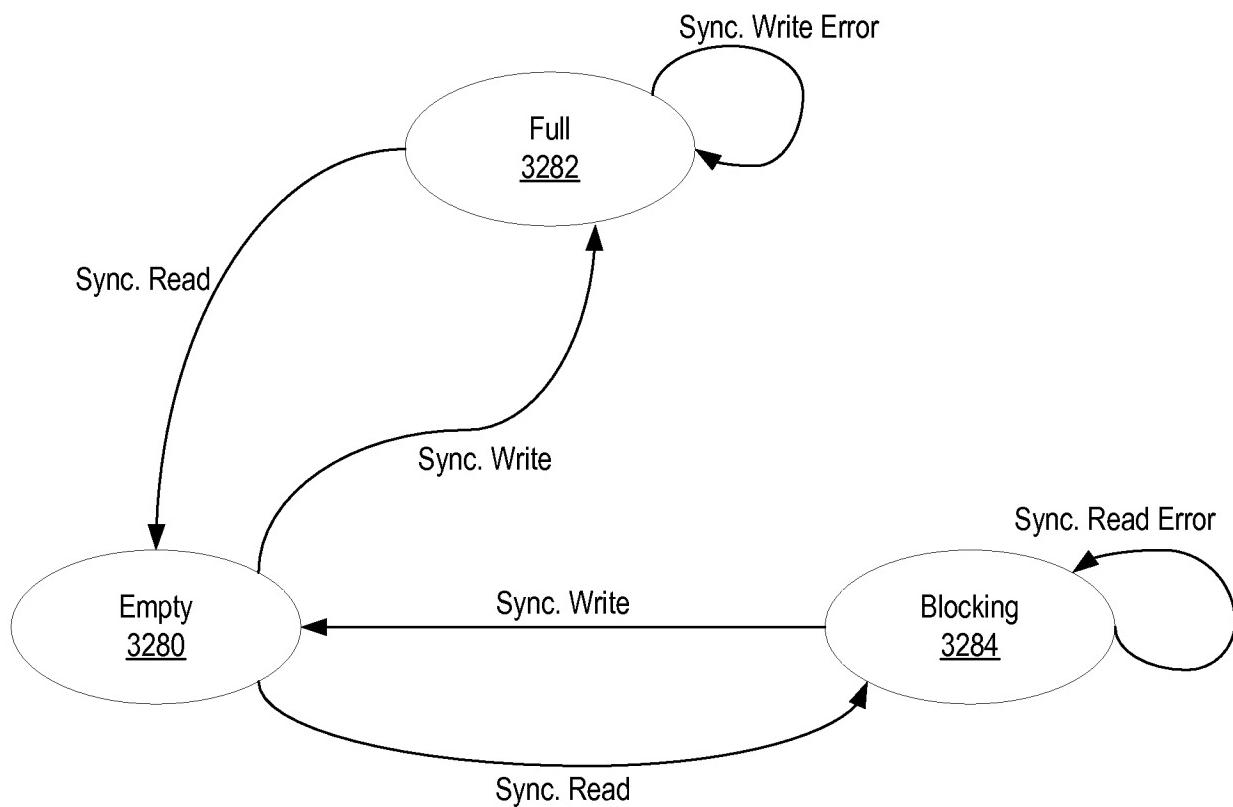


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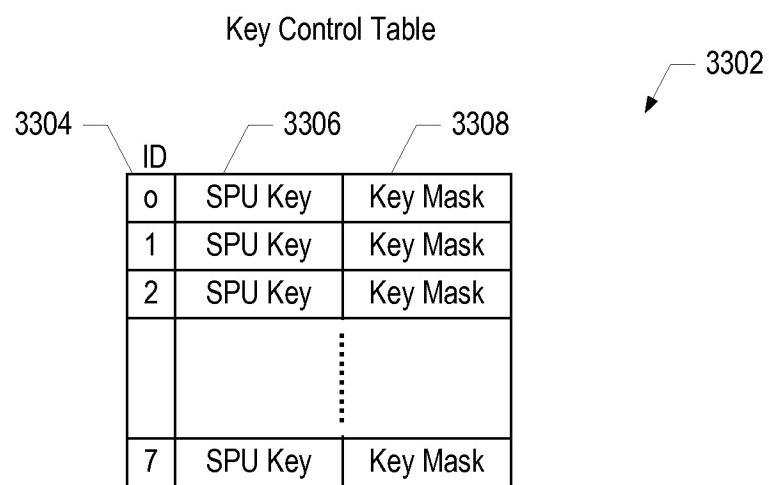


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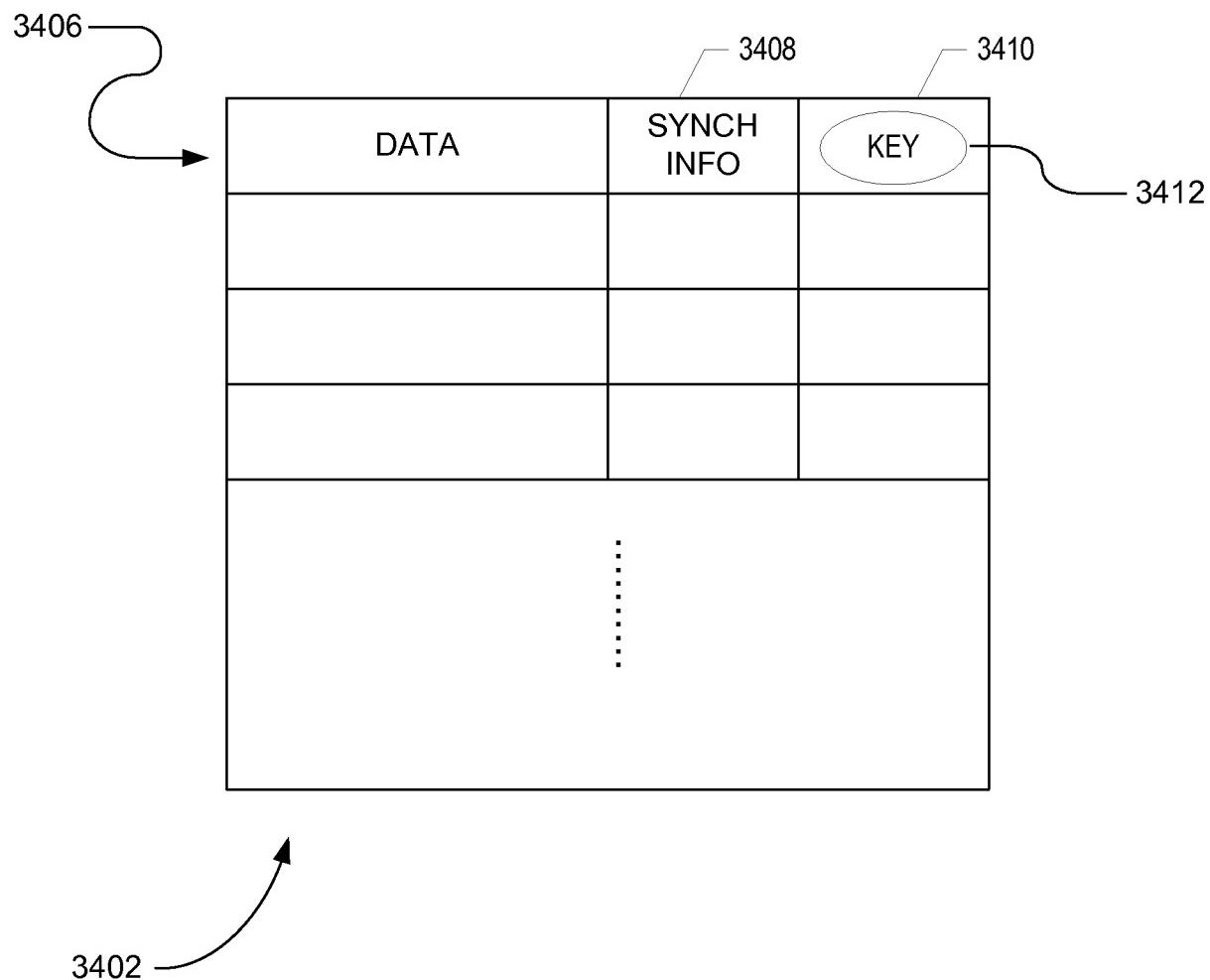


Figure 34

Memory Access Control Table

ID	Base	Size	Access Key	Access Key Mask
0	Base	Size	Access Key	Access Key Mask
1	Base	Size	Access Key	Access Key Mask
2	Base	Size	Access Key	Access Key Mask
63	Base	Size	Access Key	Access Key Mask

**Figure 35**

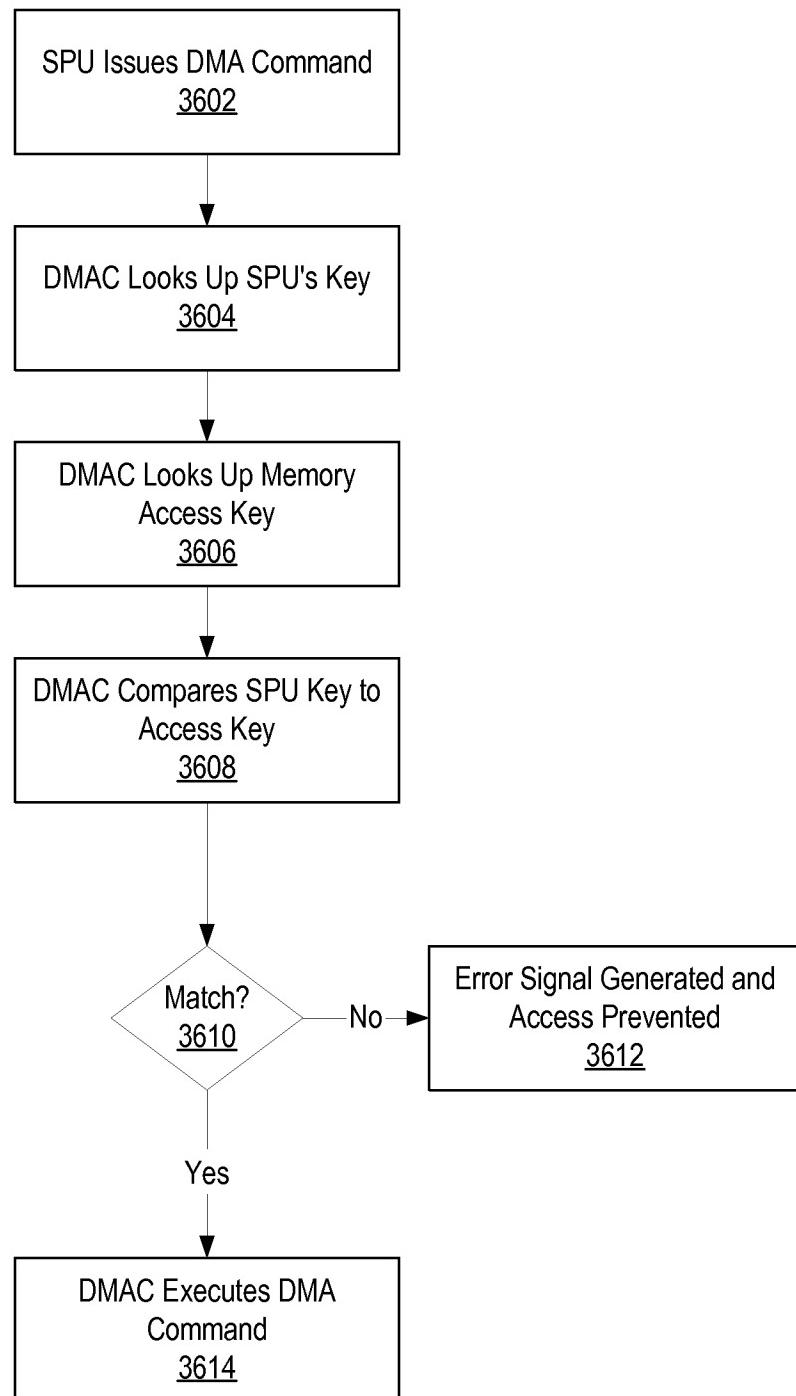
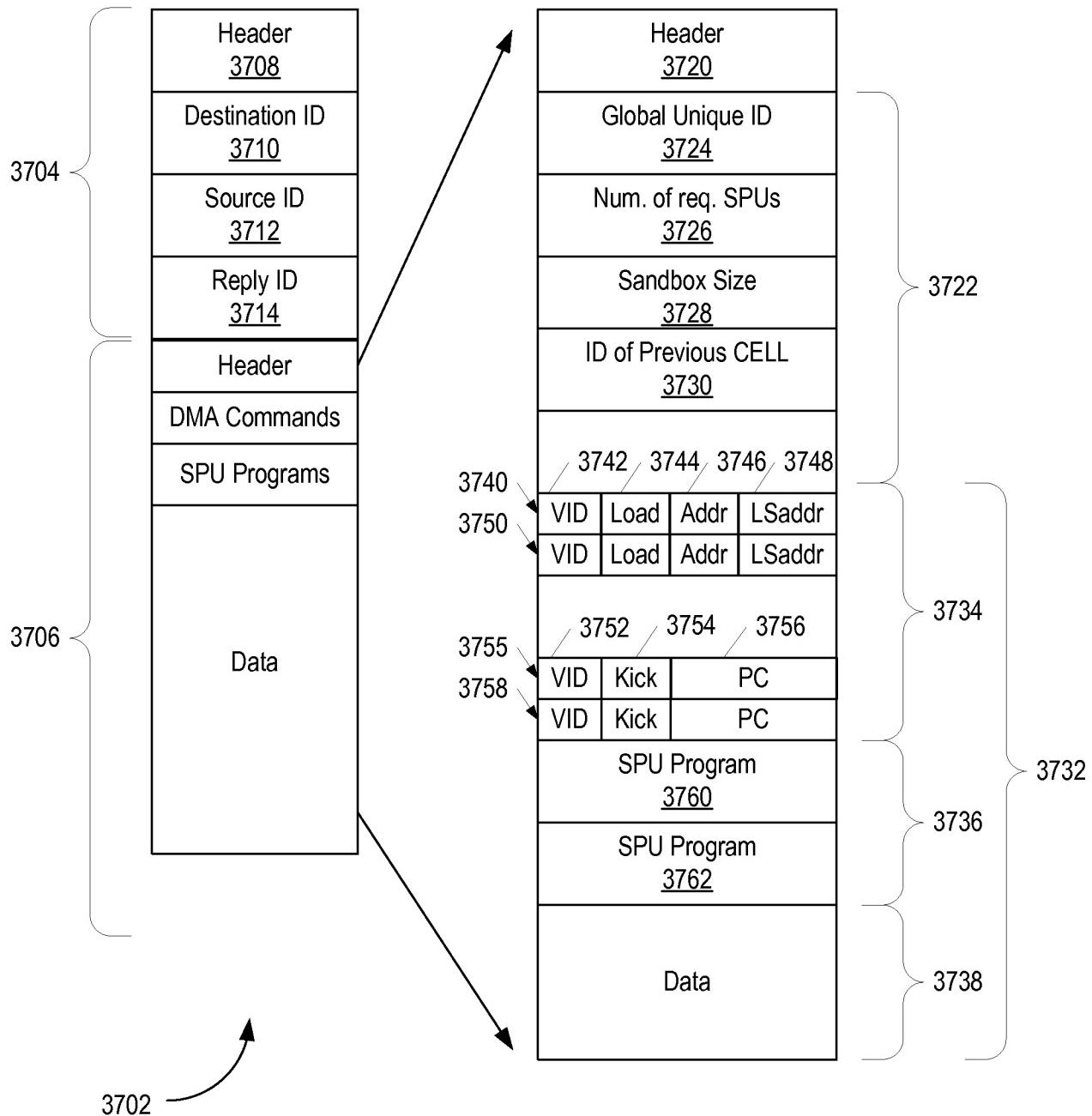
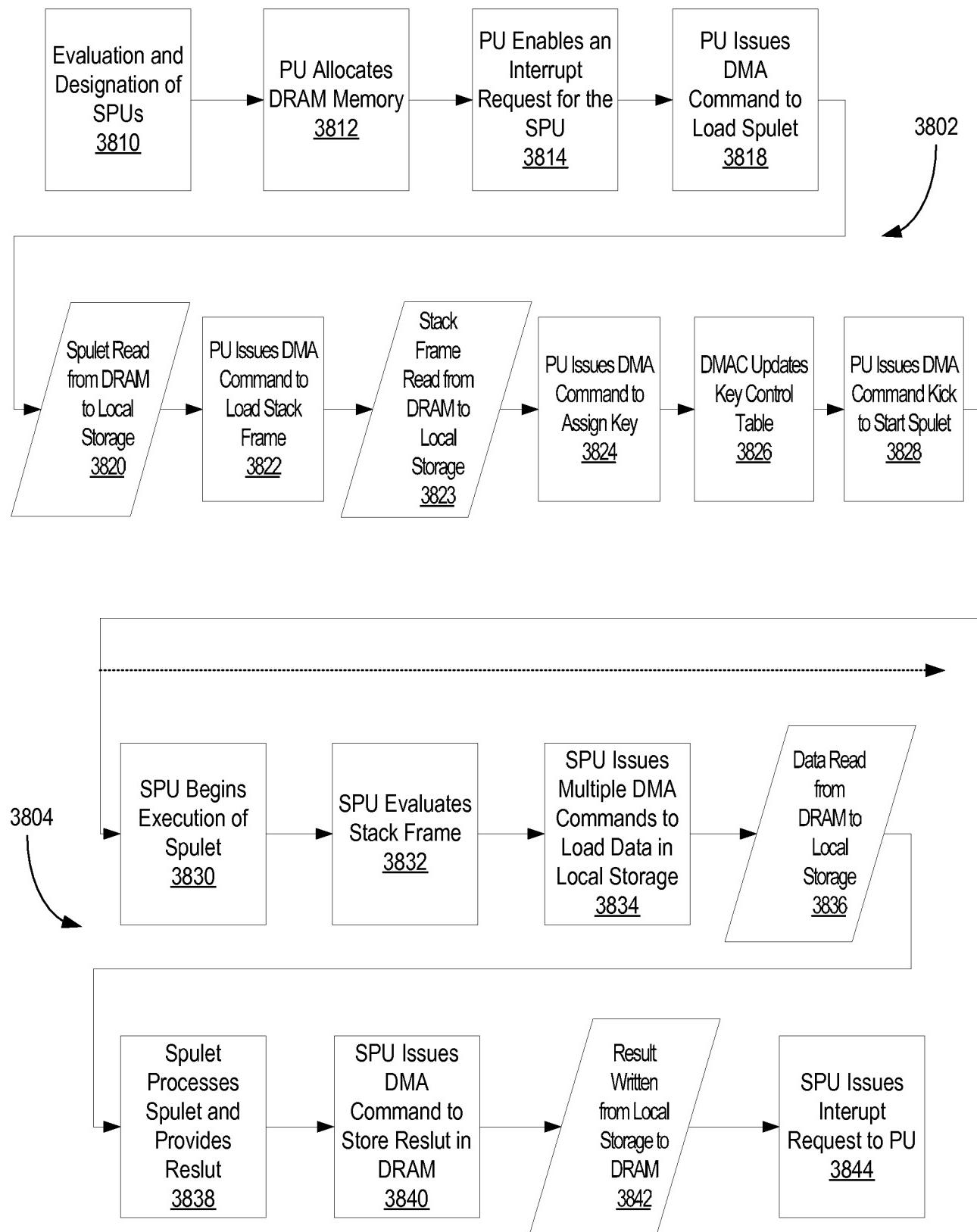


Figure 36

**Figure 37**

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**Figure 38**

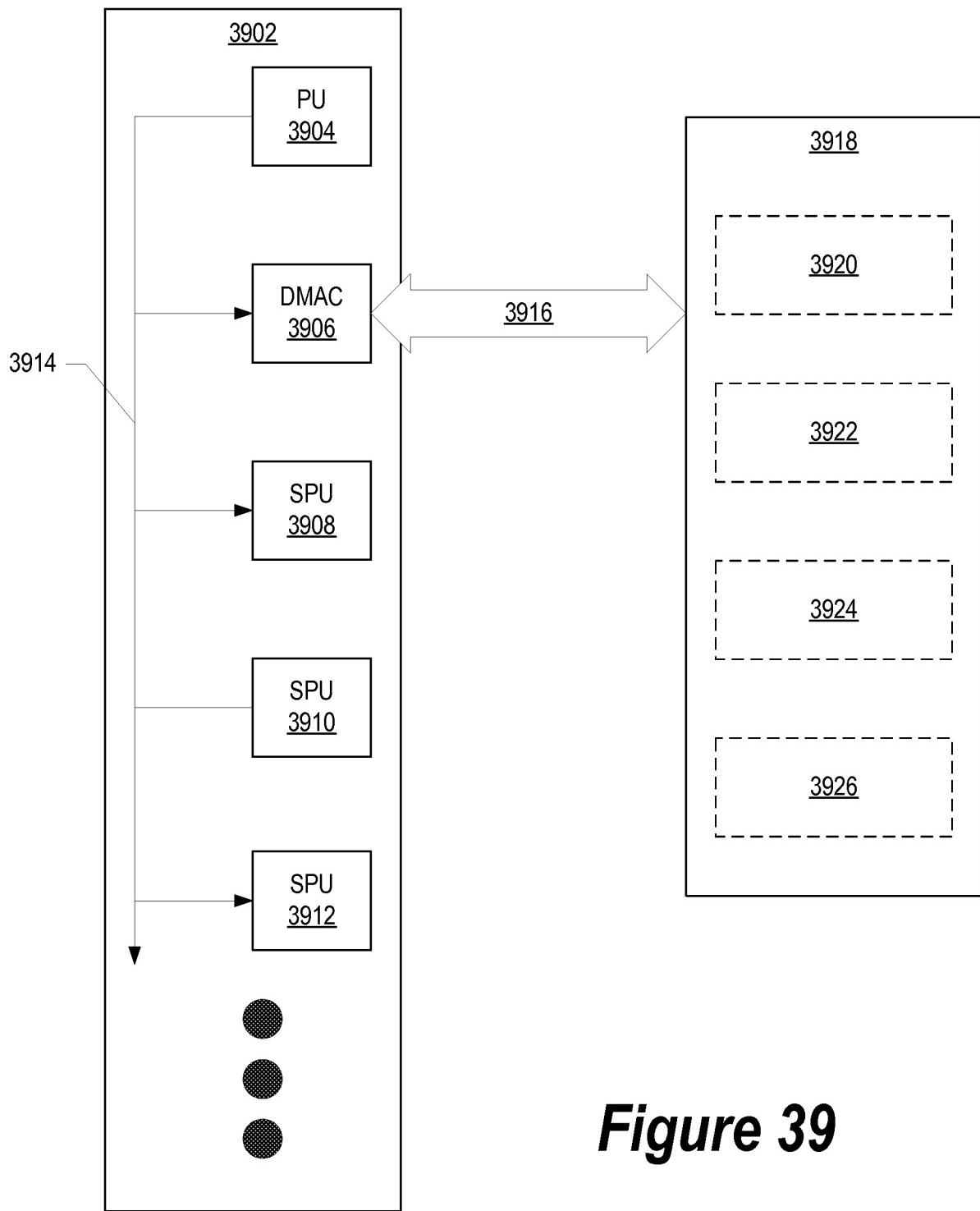
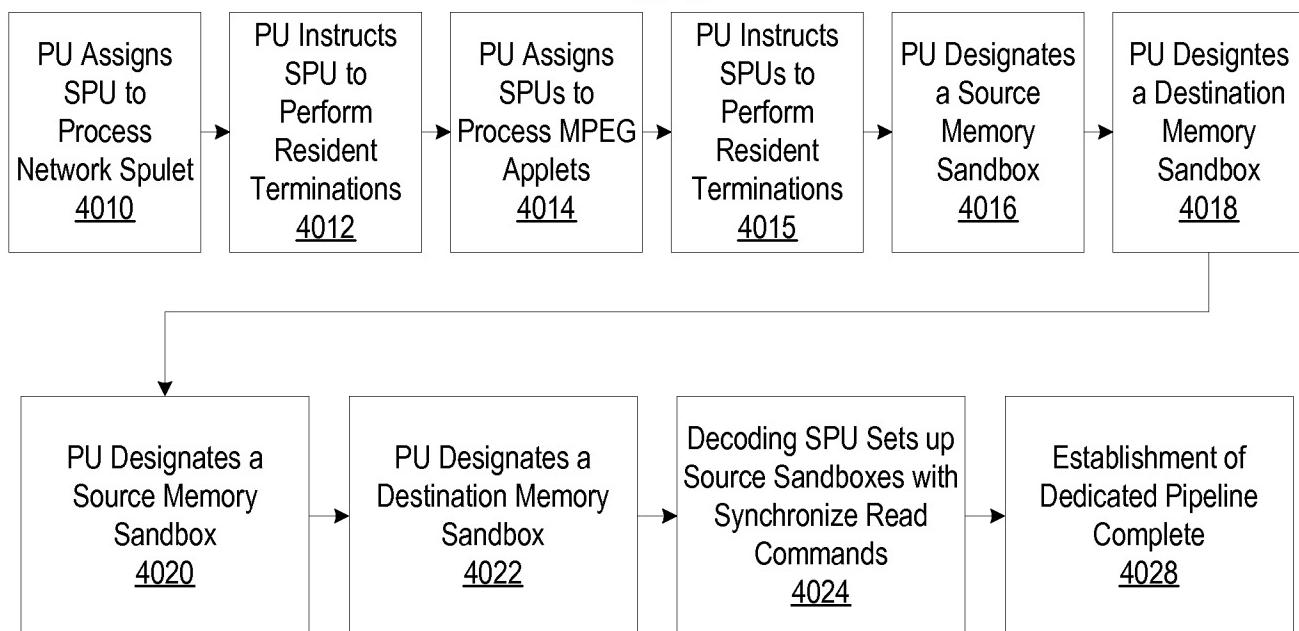
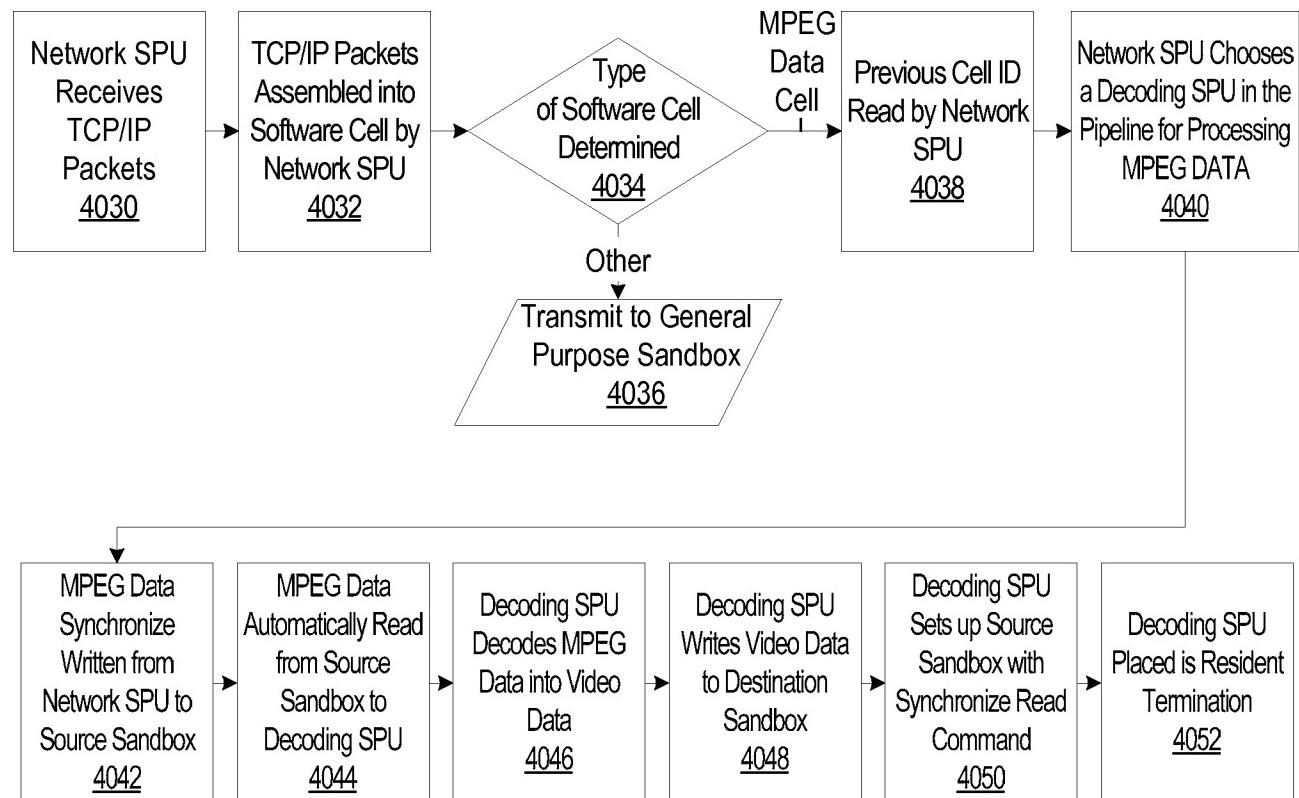


Figure 39

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**Figure 40A****Figure 40B**

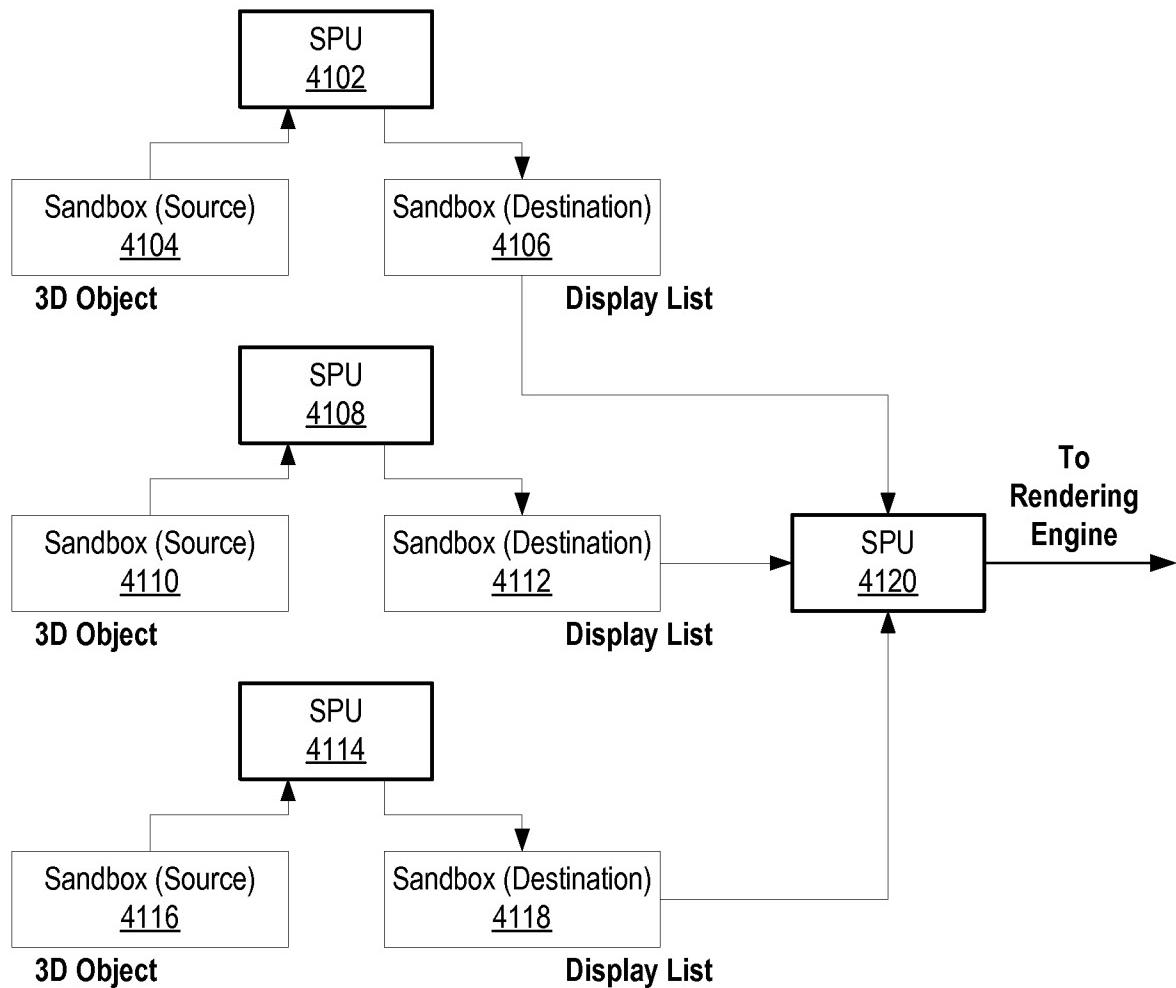
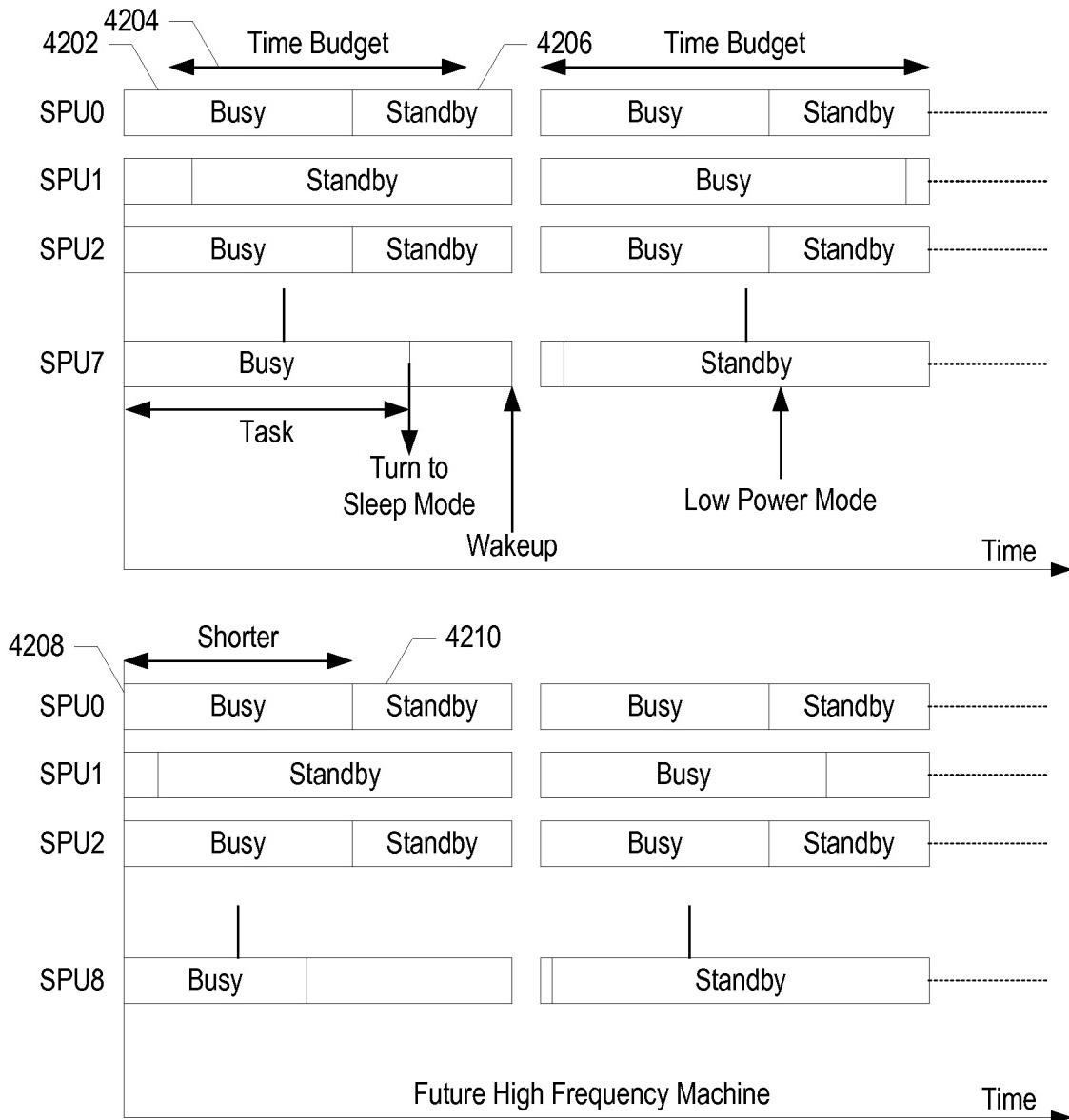
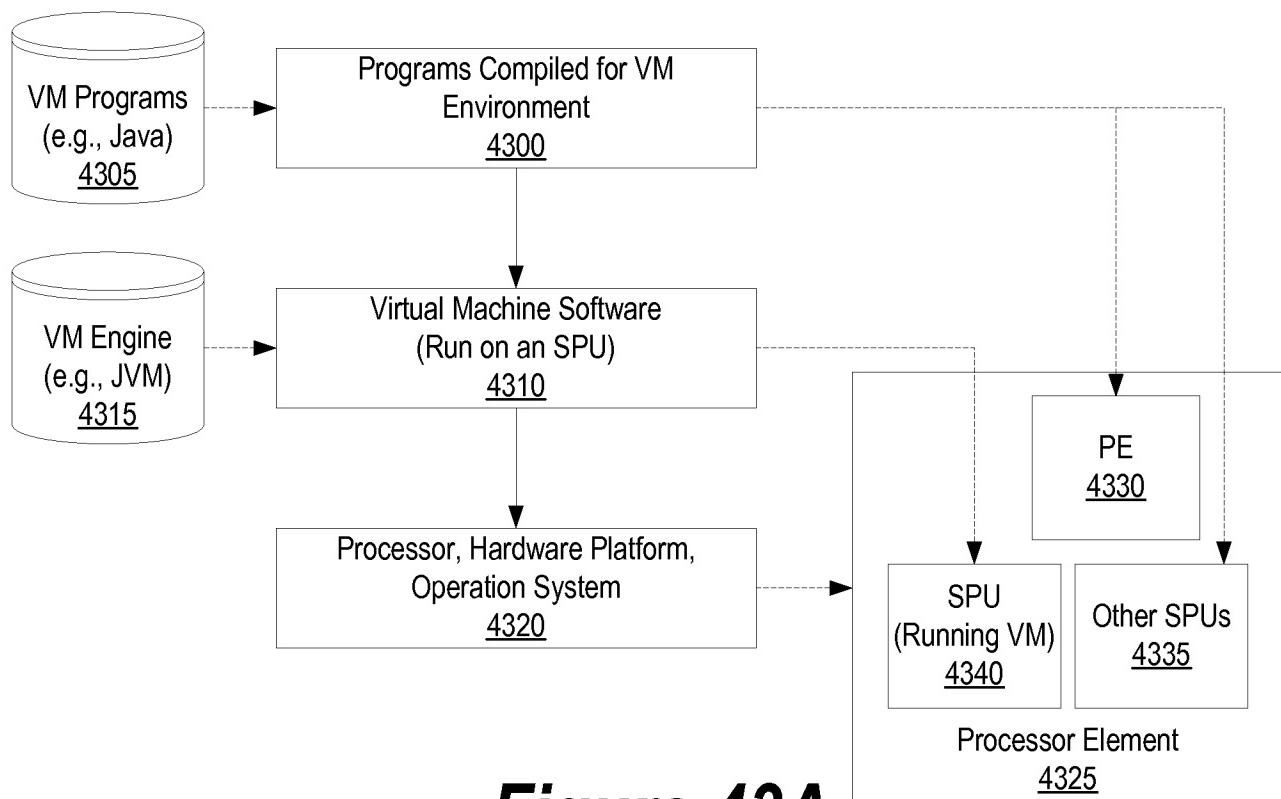
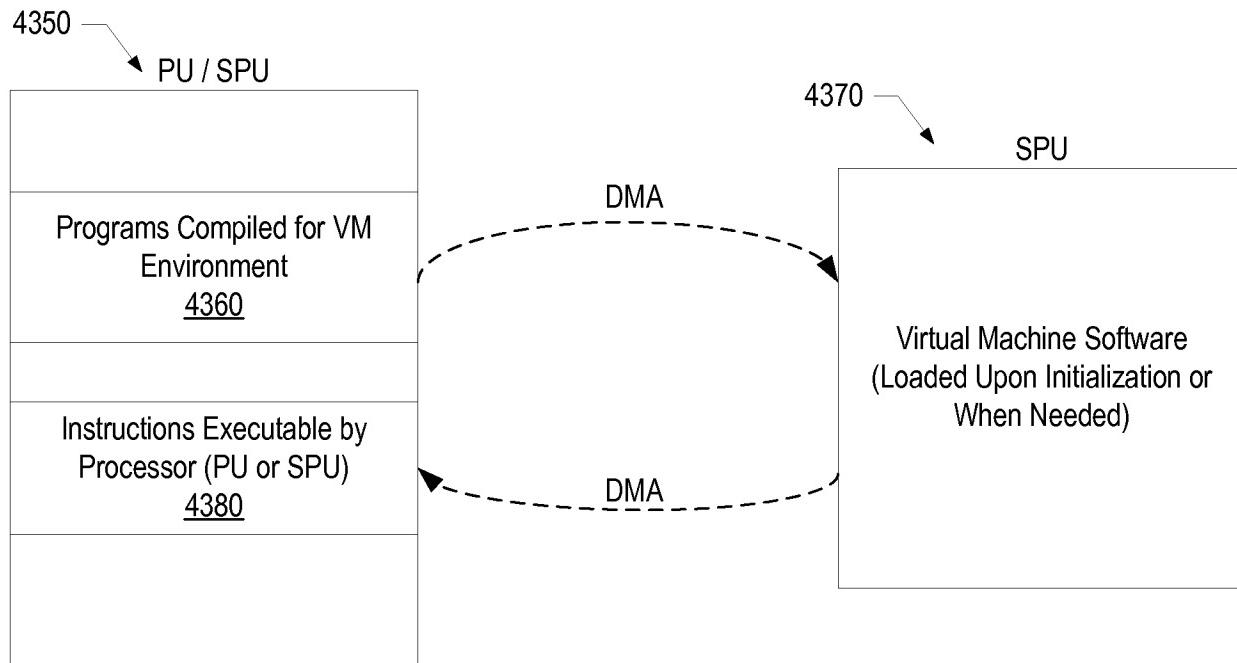
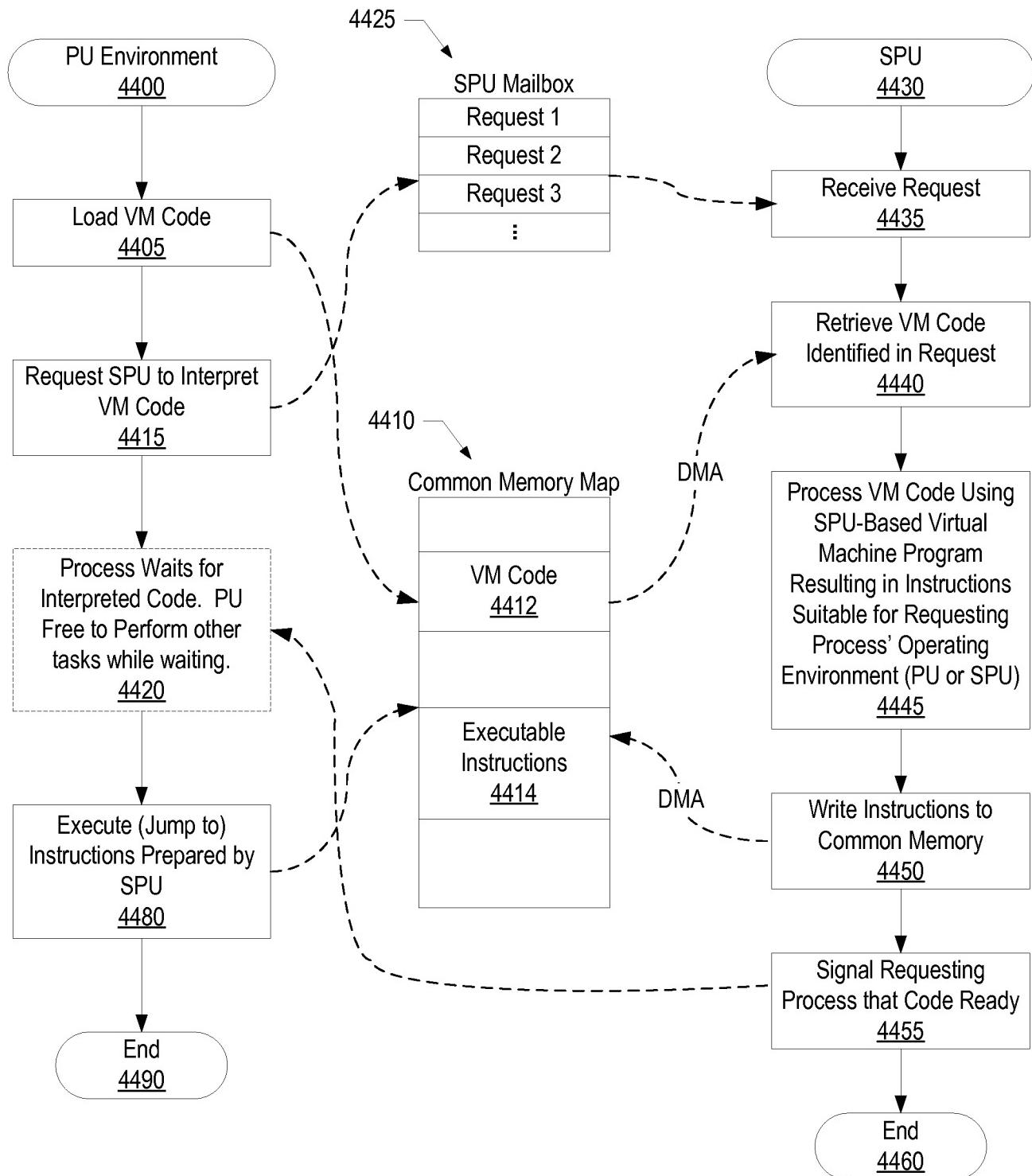


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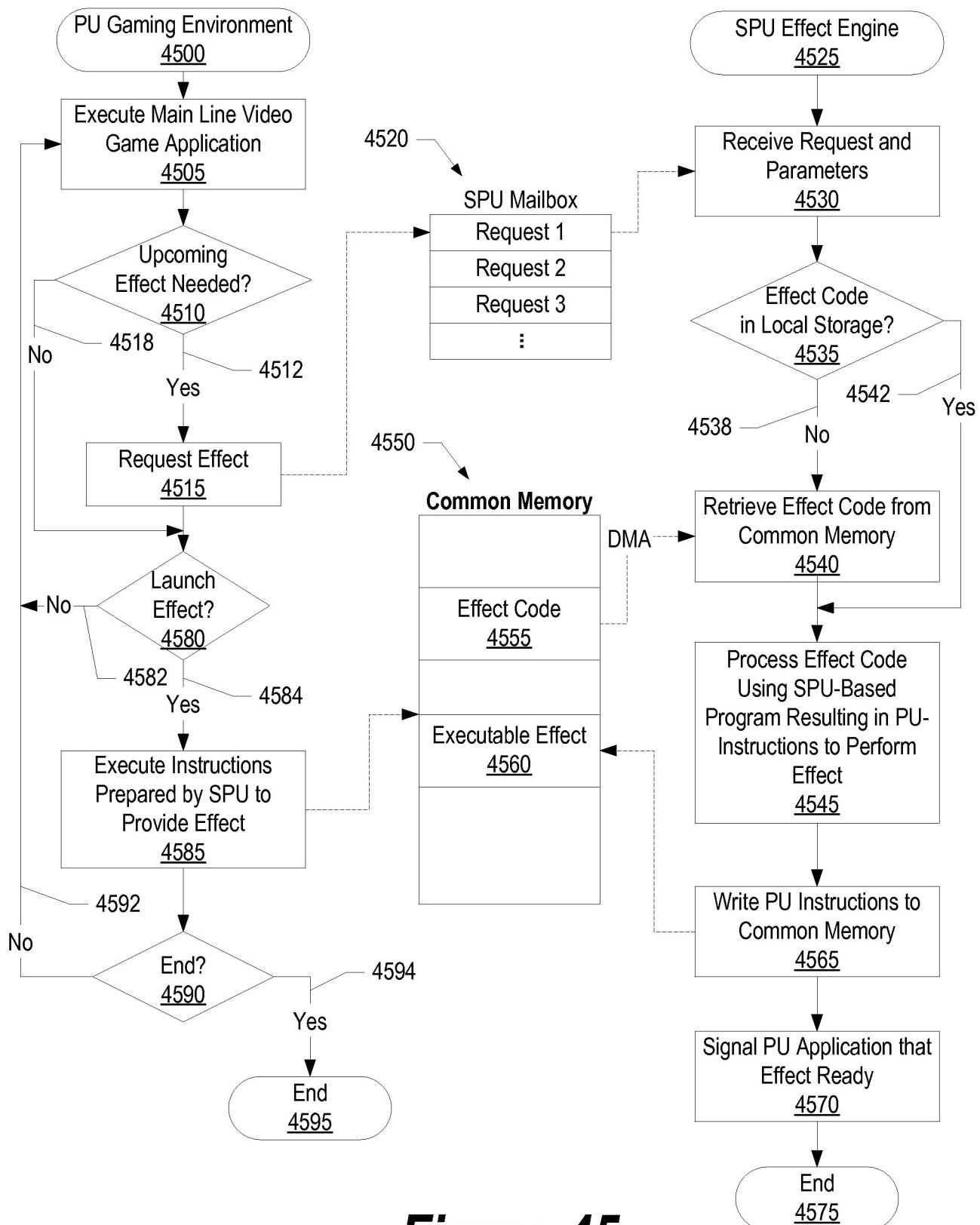
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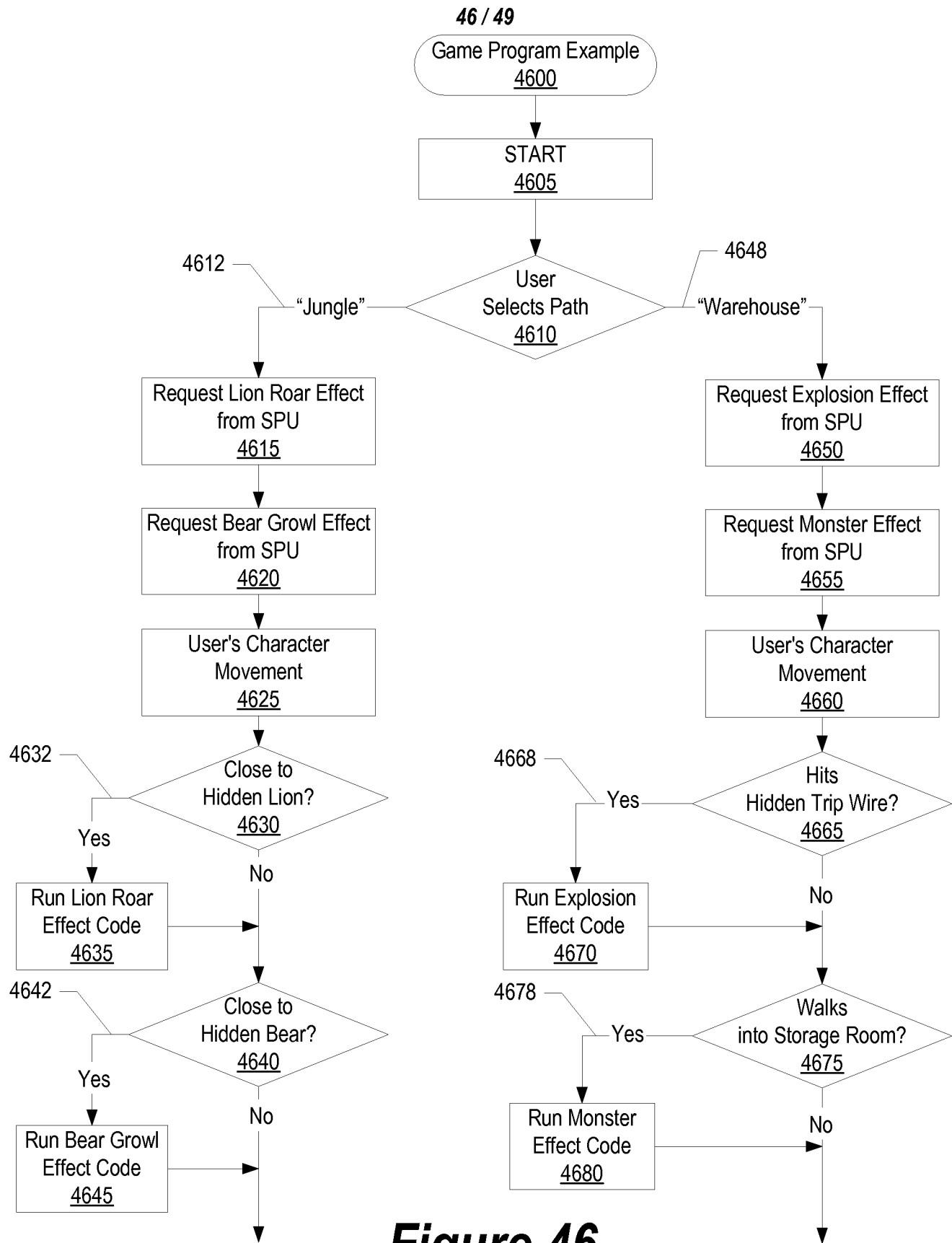
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**Figure 43A****Figure 43B**

**Figure 44**

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**Figure 45**

**Figure 46**

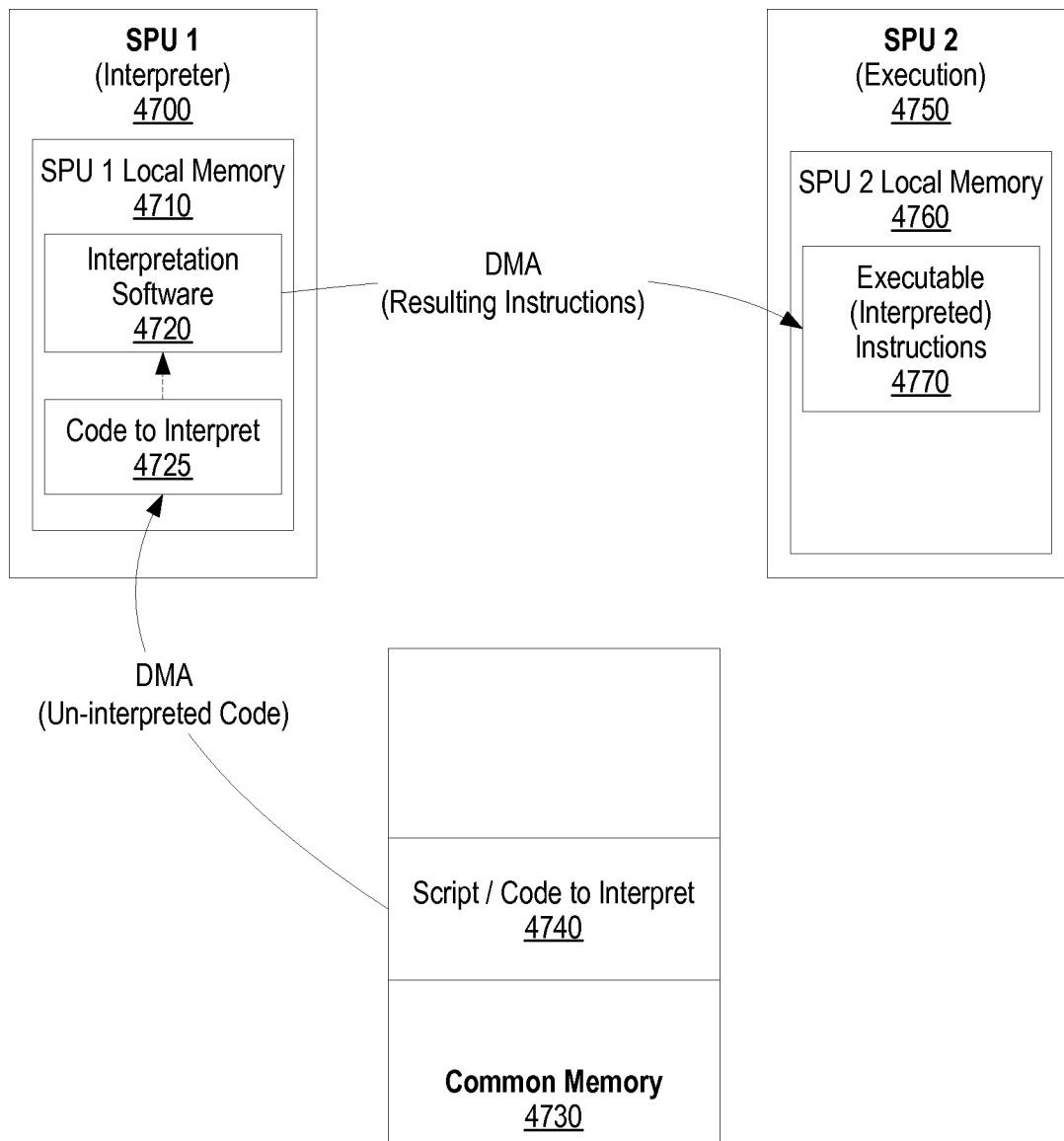
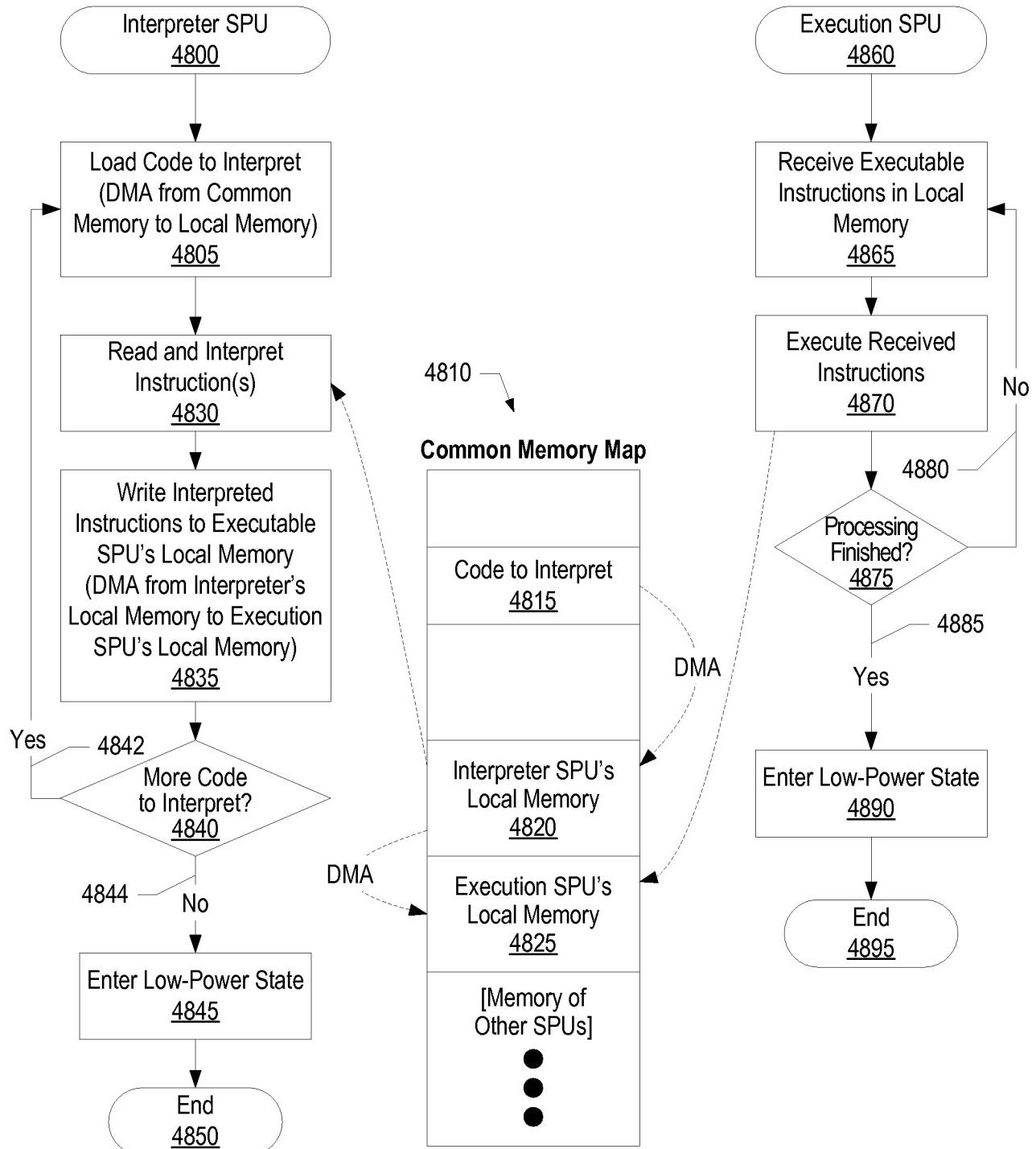
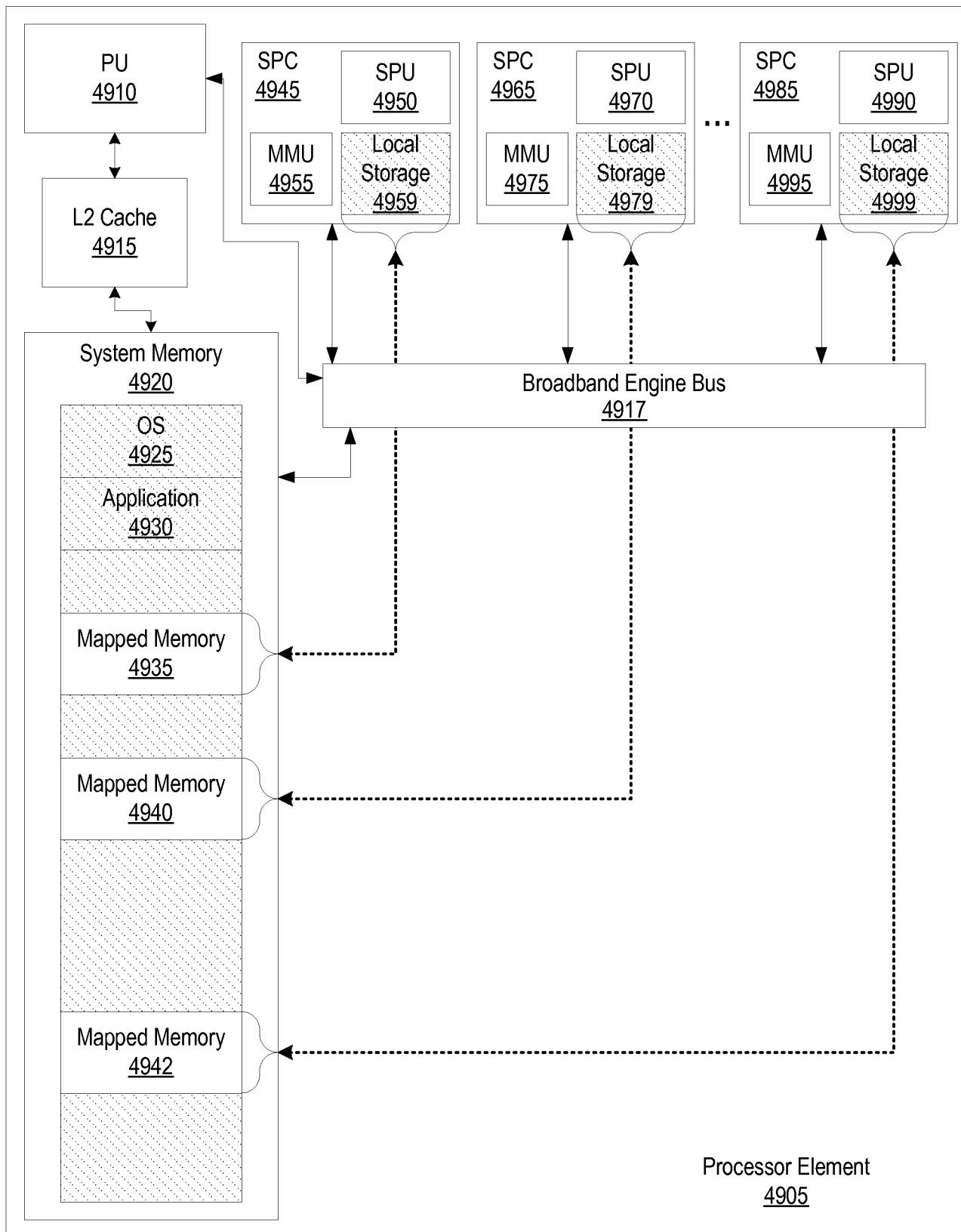


Figure 47

**Figure 48**

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**Figure 49**